



NEW IDEAS ... FOR NEW HORIZONS

MARCH 25-27, 2014

MANDALAY BAY RESORT AND  
CONVENTION CENTER  
LAS VEGAS, NEVADA



The leading company of conductive and insulating materials for electronics  
**NAMICS CORPORATION**



***Packaging Materials for 2.5/3D***

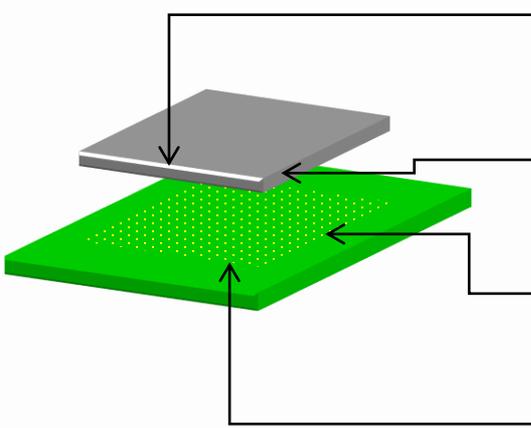
*Brian Schmaltz / NAMICS Western Area Manager / [schmaltz@namics-usa.com](mailto:schmaltz@namics-usa.com)*

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- Packaging Trend
- 3D Integration
- Underfill Opportunities in 3D Packaging Solution
- CUF Technology Progression
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- Overview of Pre-Applied Underfill
- NCP/TCCUF/NCF/BNUF Technology & Line-Up



## Technological Transition in FCBGA Packages



The diagram shows a 3D perspective of a die (grey) mounted on a substrate (green). Arrows point from the die and substrate to the corresponding rows in the table below.

Year	2012	2013	2014	2015
<b>Process generation &amp; LowK generation (nm)</b>	32/28		22/20	
<b>Maximum Die size (mm)</b>	20x20 to 26x26 – FC-BGA 10x10 to 15x15 – FC-CSP			
<b>Interconnection</b>	Eu		Lead free	
<b>Interconnection pitch (um)</b>	130	120	Cu-Pillar <120	

### Trend

- Using fragile ultra low-k layers →
- Using lead-free solder with high rigidity →
- Using Cu pillar →

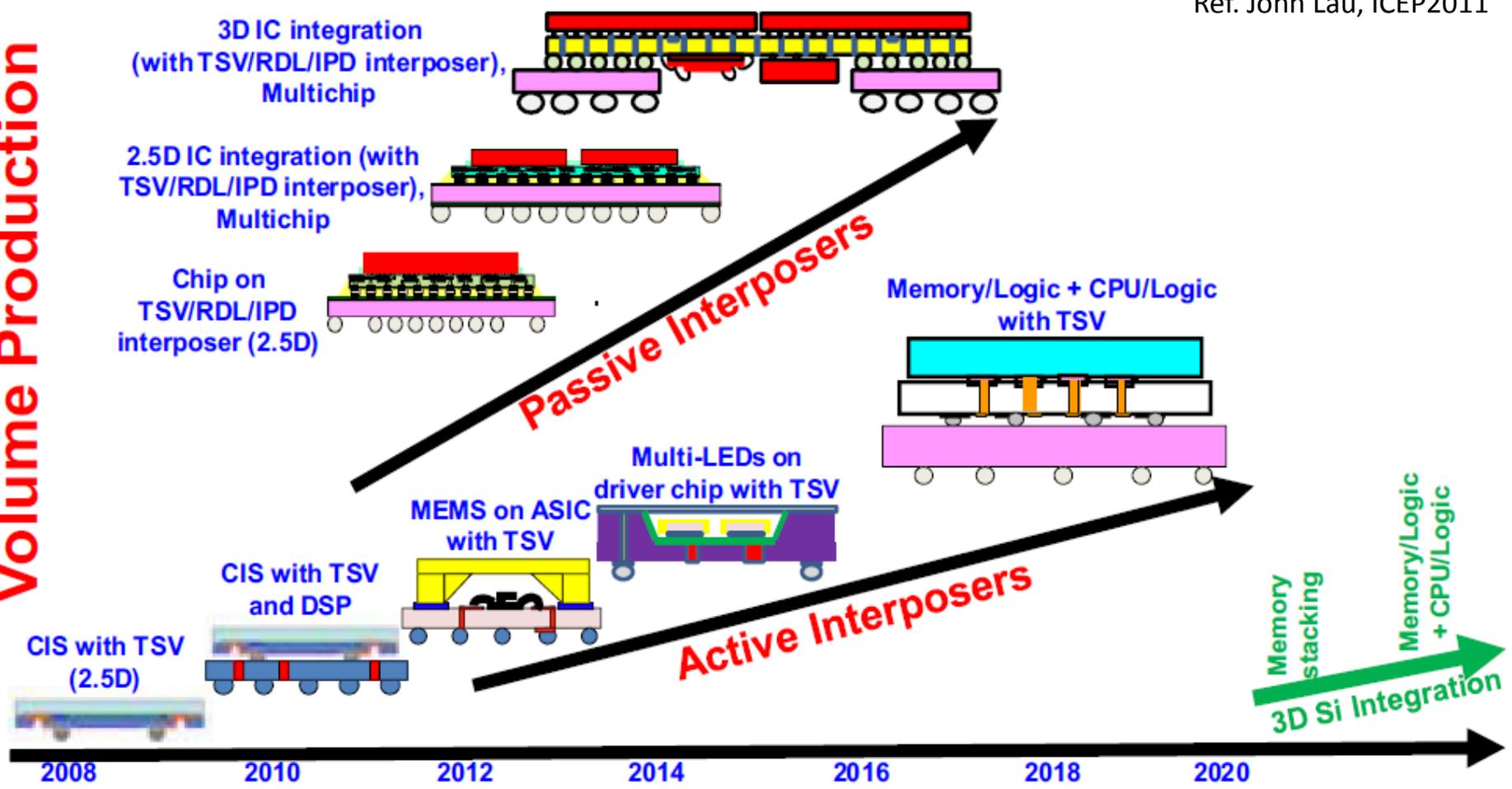
### Concern

- Low-k crack
- Bump crack
- Filler separation

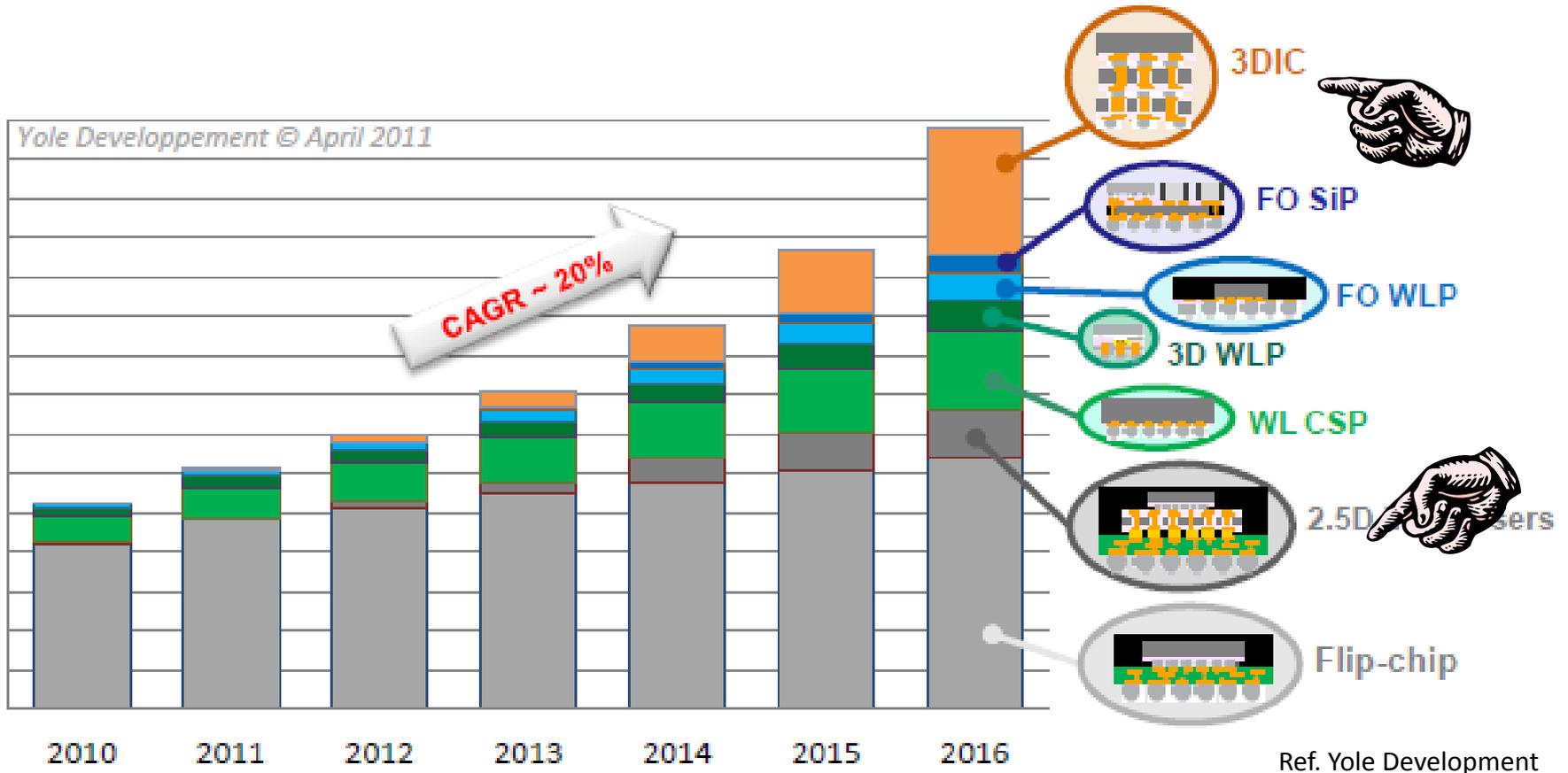
# 2.5D / 3D Integration Roadmap

Ref. John Lau, ICEP2011

**Volume Production**



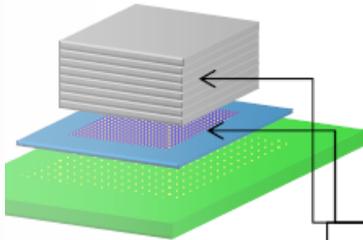
# Global Wafer-Level-Packaging Trend



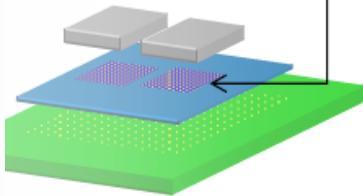
Wafer-level-packages have emerged as the fastest growing semiconductor packaging technology with more than 27% CAGR [compound annual growth rate] in unit shipments over the next 5 years to come.

# NextGen Package Trend – Material Properties

3D TSV



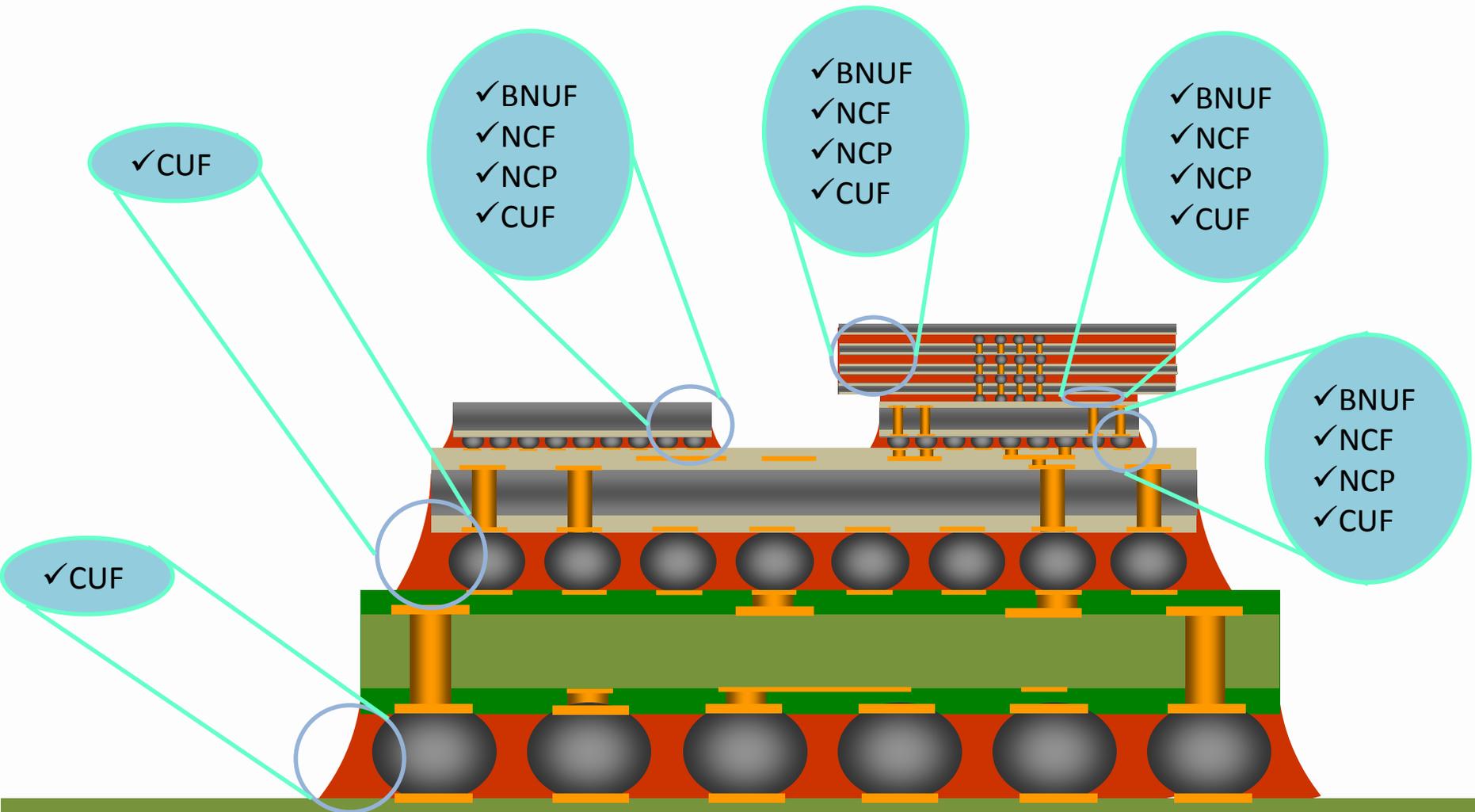
2.5D



<i>2.5D/3D Packaging trend</i>				
Year	2012	2013	2014	2015
<i>Microbump gap (um)</i>	20 - 30			<20

<i>Underfill development target for 2.5D/3D Packaging</i>				
Year	2012	2013	2014	2015
<i>Filler size (um)</i>	0.3 ~ 0.6			0.3
<i>CTE (ppm/C)</i>	30 ~ 40			20 ~ 30
<i>Thermal conductivity (W/m.K)</i>	< 2.0			> 2.0
<i>α ray emission (count/cm<sup>2</sup>h)</i>	<0.001			
<i>Underfilling method</i>	CUF / VCUF			NCF

# Opportunities in 3D Packaging Solution



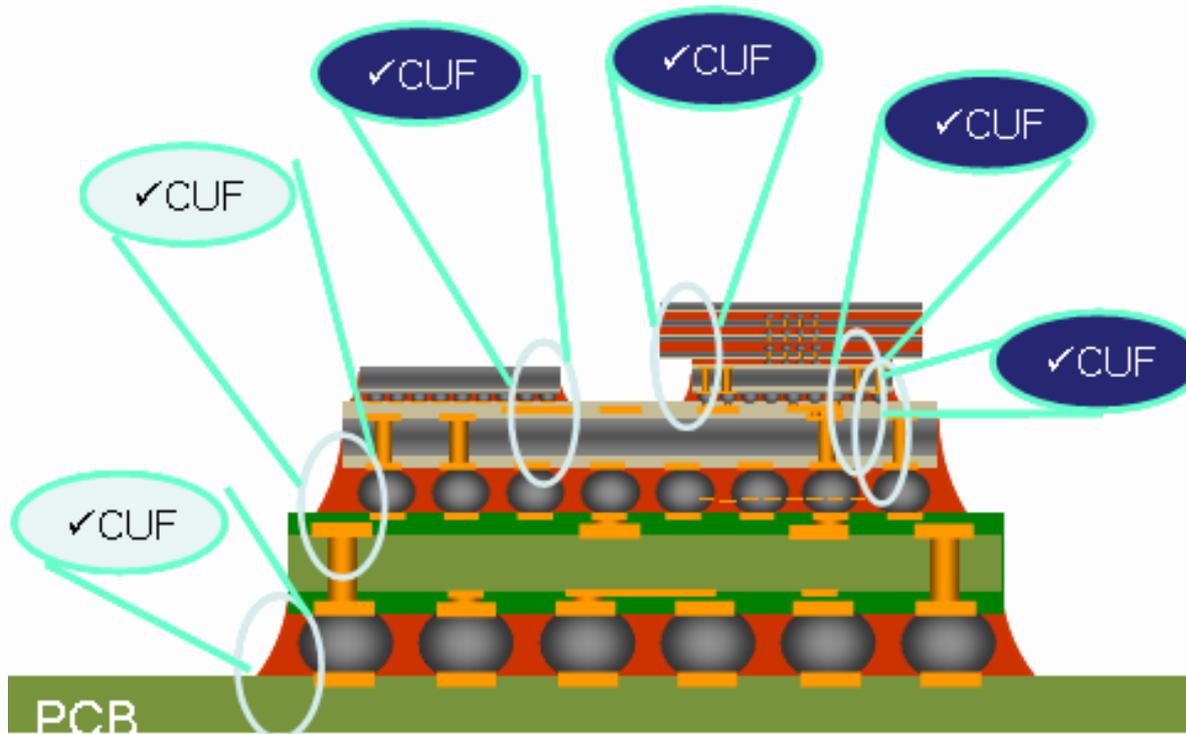
# ***Epoxy Line-Up by Application Method***

Type	Post-dispensing			Pre-applied					
	CUF (Capillary UnderFill)			NCP (Non Conductive Paste)		B-stagable NUF (No flow UnderFill)		NCF (Non-Conductive Film)	
Application by	Dispensing			Dispensing		Dispensing		Spin	Lamination
	Air	Jet	Vacuum	Air	Jet	Air w/ special nozzle	Spray		
Apply to	Die attached substrate			Substrate		Substrate or Wafer		Wafer	Substrate or Wafer
Flux process	Required			Not required					
Die attachment process	Mass reflow or TCB			TCB (Thermal Compression Bonding)					

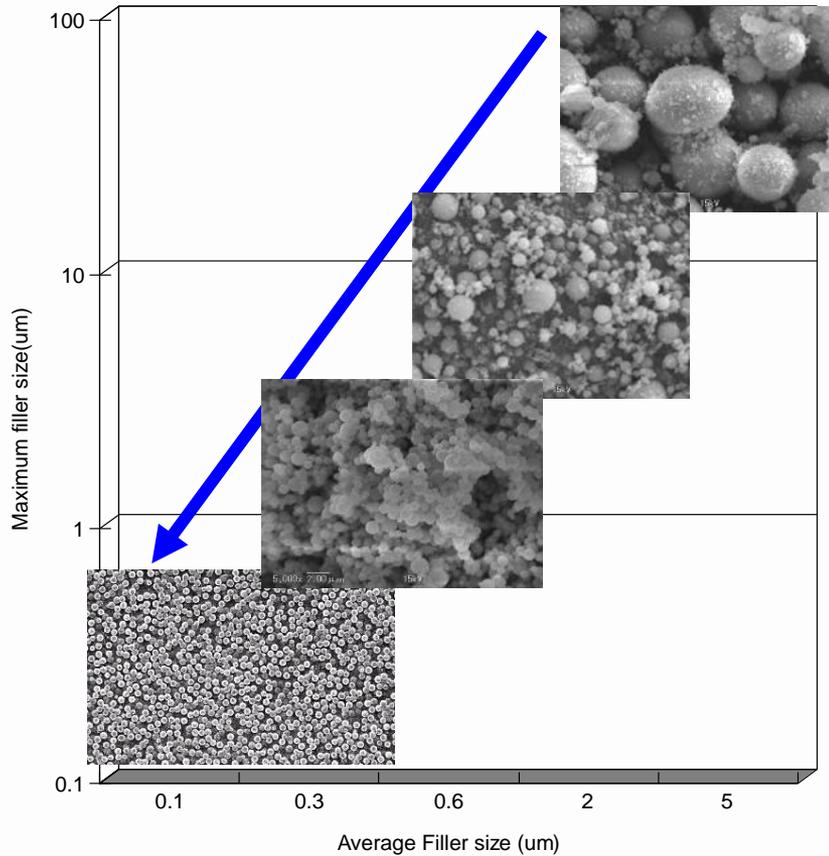
# Capillary Underfill (CUF) for 3D Packaging

## Key properties

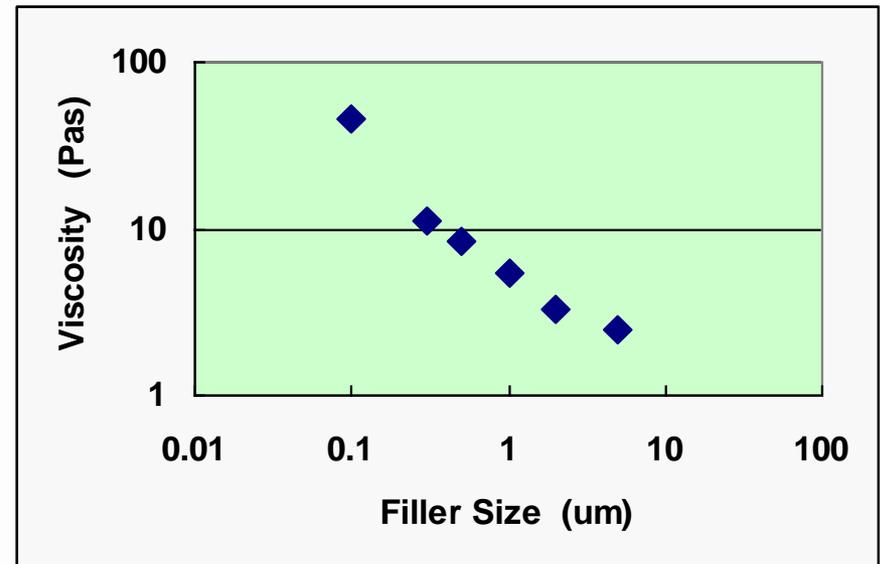
- ❑ *High Flow-ability for Narrow Gap*
- ❑ *High precision dispensing*
- ❑ *Vacuum assisted process*



# Filler Design

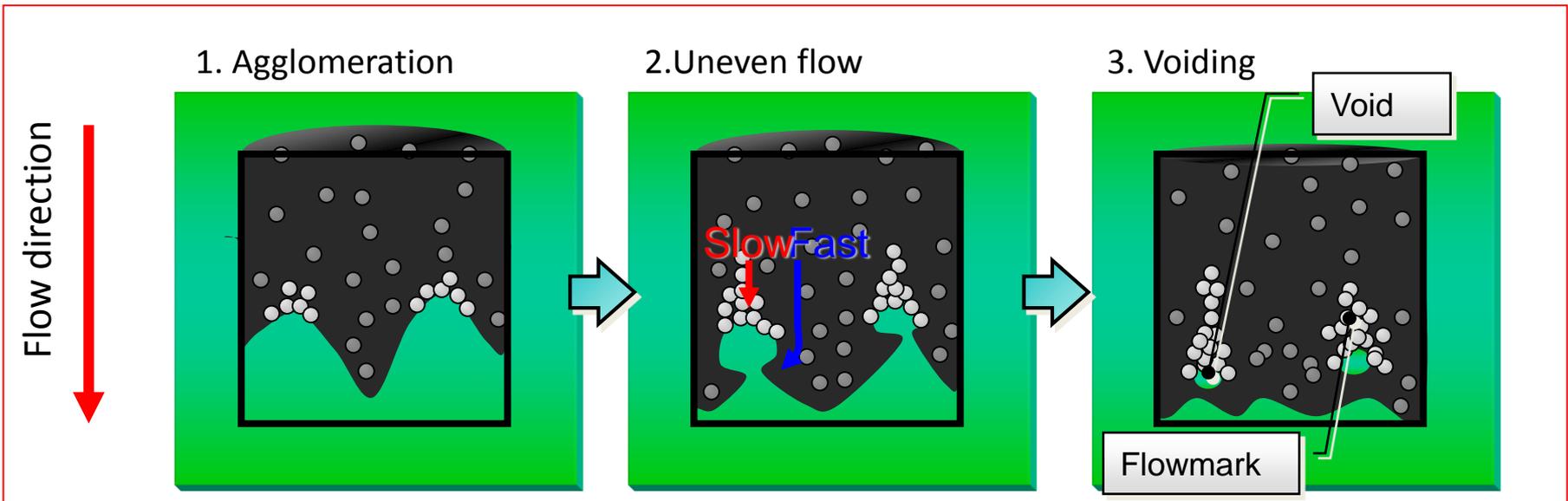
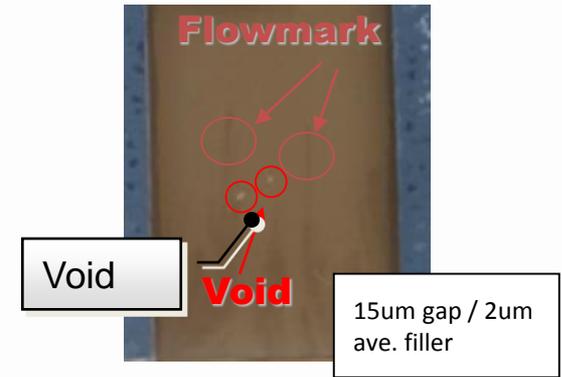
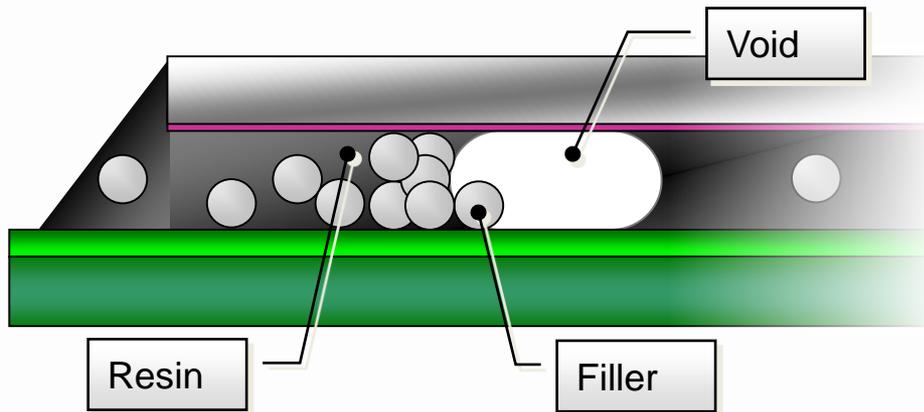


For fine pitch and narrow gap, filler size must be considered for good capillary flow.



Its drawback is that fine filler significantly increase viscosity of underfill.

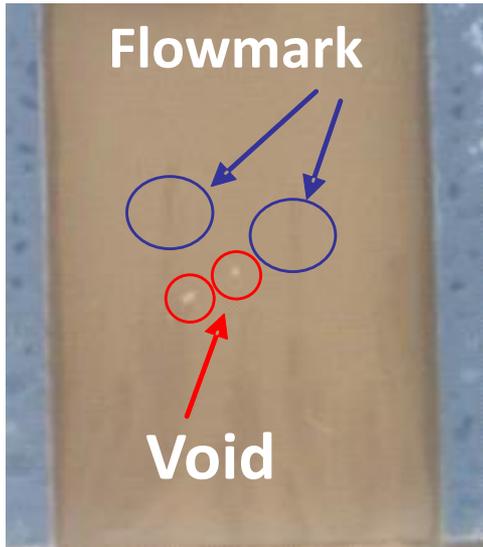
# Root Cause for Voiding with Larger Size Filler



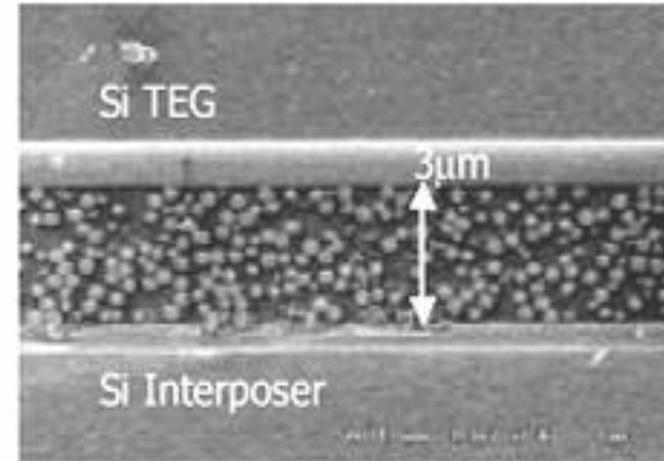
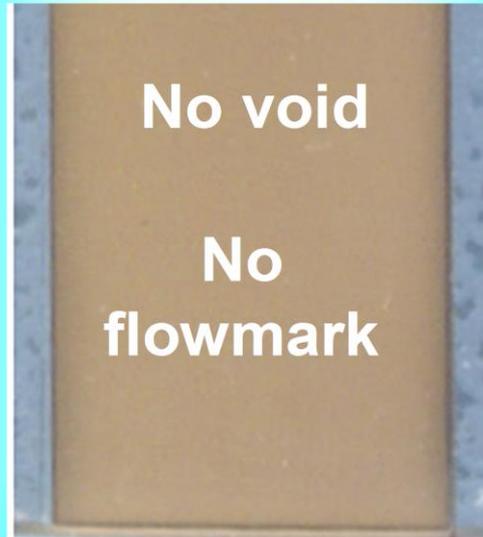
## High Flowability for Narrow Gap

Flow comparison:

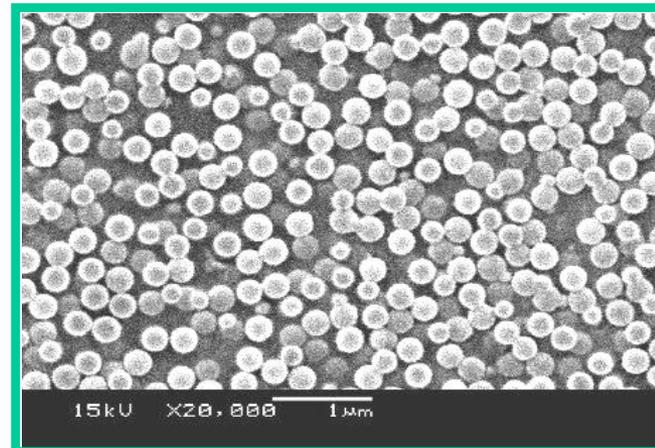
Filler size  
2 $\mu$ m ave.



Filler size  
0.3 $\mu$ m ave.

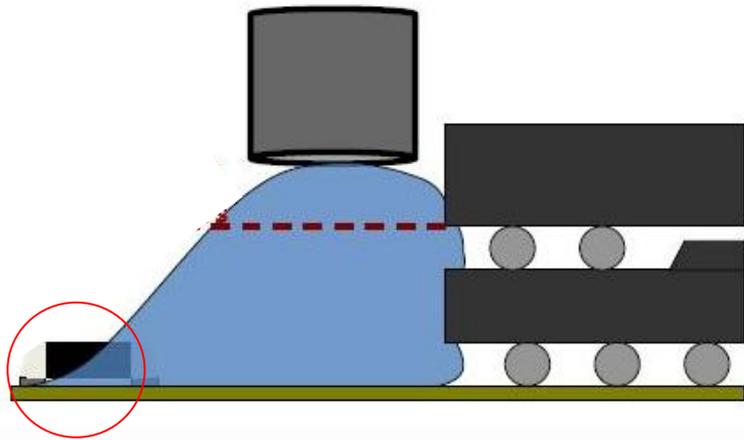


SEM Photo of cross section



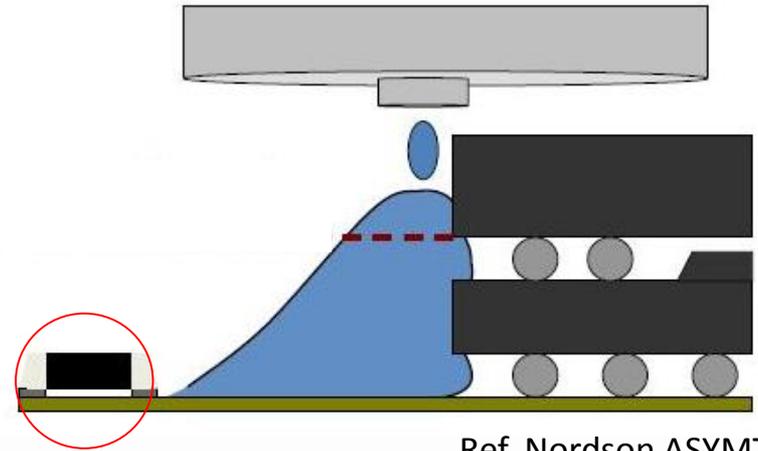


## ***Dispensing Method; Needle or Jet ?***



### ***Needle dispensing***

- ✓ Keep out zone (KOZ) is getting smaller.
- ✓ No space for needle nozzle.



Ref. Nordson ASYMTEK

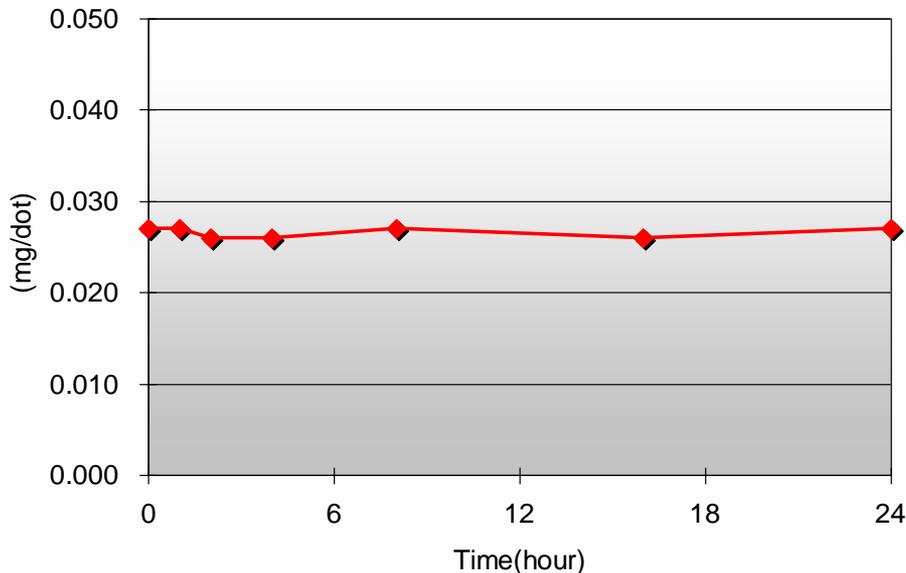
### ***Jet dispensing***

- ✓ Jetting can maintain minimum KOZ with accuracy.
- ✓ Requires less space than needle dispensing

## High Repeatability Dispensing

Underfill material	Dispensed weight		
	0h	16h	24h
NAMICS	0.027mg/dot	0.026mg/dot	0.027mg/dot

Namics underfill shows stable performance with jet dispensers.



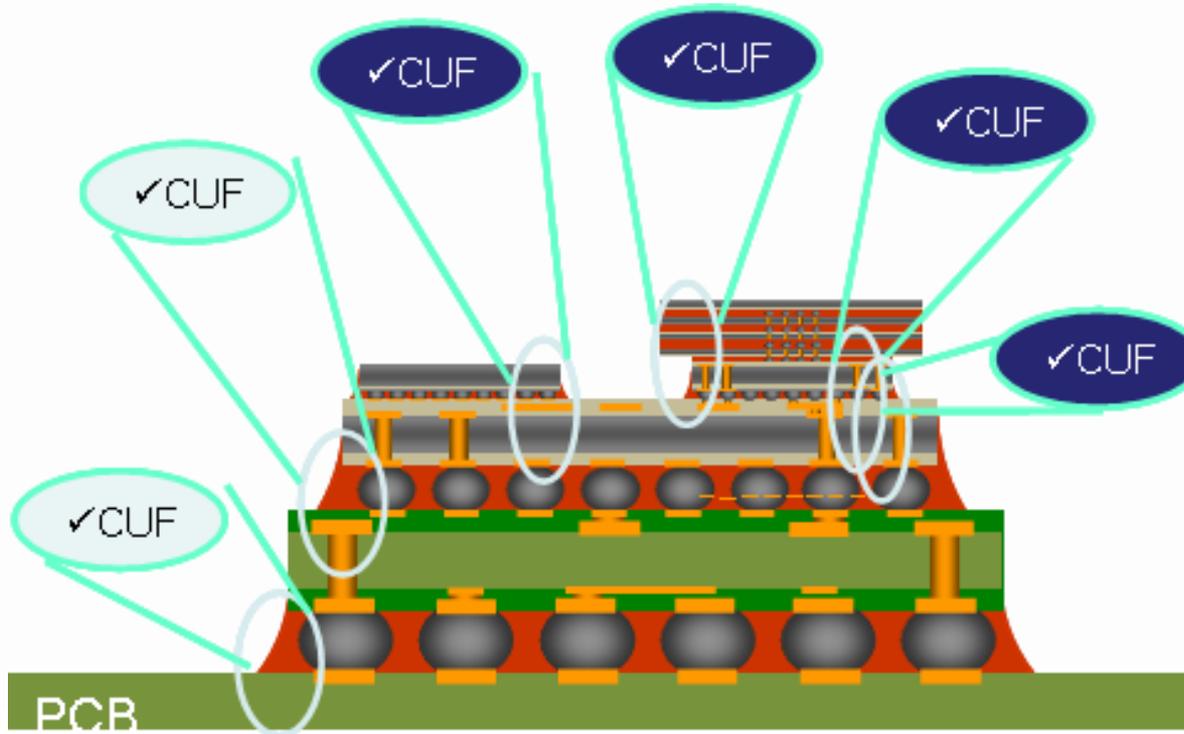
Conditions	
Instrument	Asymtek DJ9000 Jet Dispenser
Fluid pressure	0.02 MPa
Nozzle temperature	70 deg.C
Stage temperature	90 deg.C

**Namics Underfill is designed for jet dispensing.**

# Capillary Underfill (CUF) for 3D Packaging

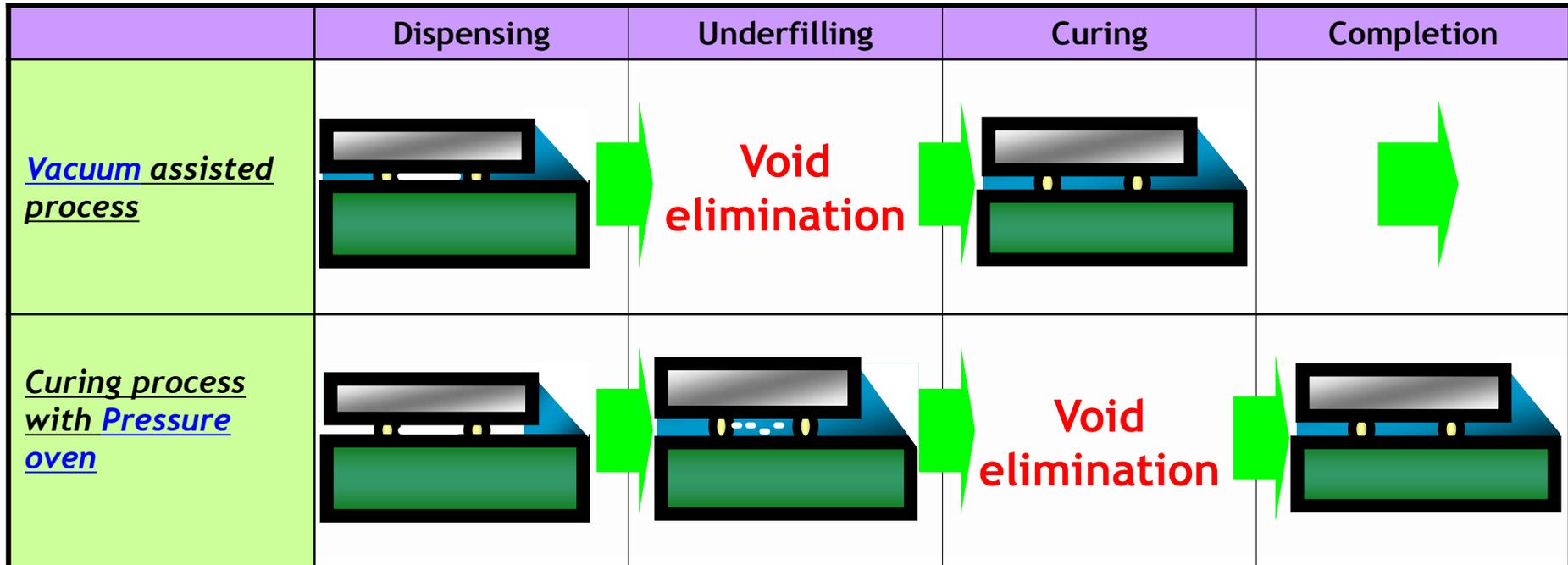
## Key properties

- ★ High Flow-ability for Narrow Gap
- ★ High precision dispensing
- Vacuum assisted process



# Void Elimination Process / Pressure Oven

	<i>Vacuum assisted process</i>	<i>Curing process with Pressure oven</i>
Equipment	Vacuum system	Pressure oven
Void reduction process	At underfilling	At curing
A sort of void to eliminate	Capture	Capture, Moisture
Size of void to eliminate	Large ~ Small	Small
Underfill	VCUF	CUF

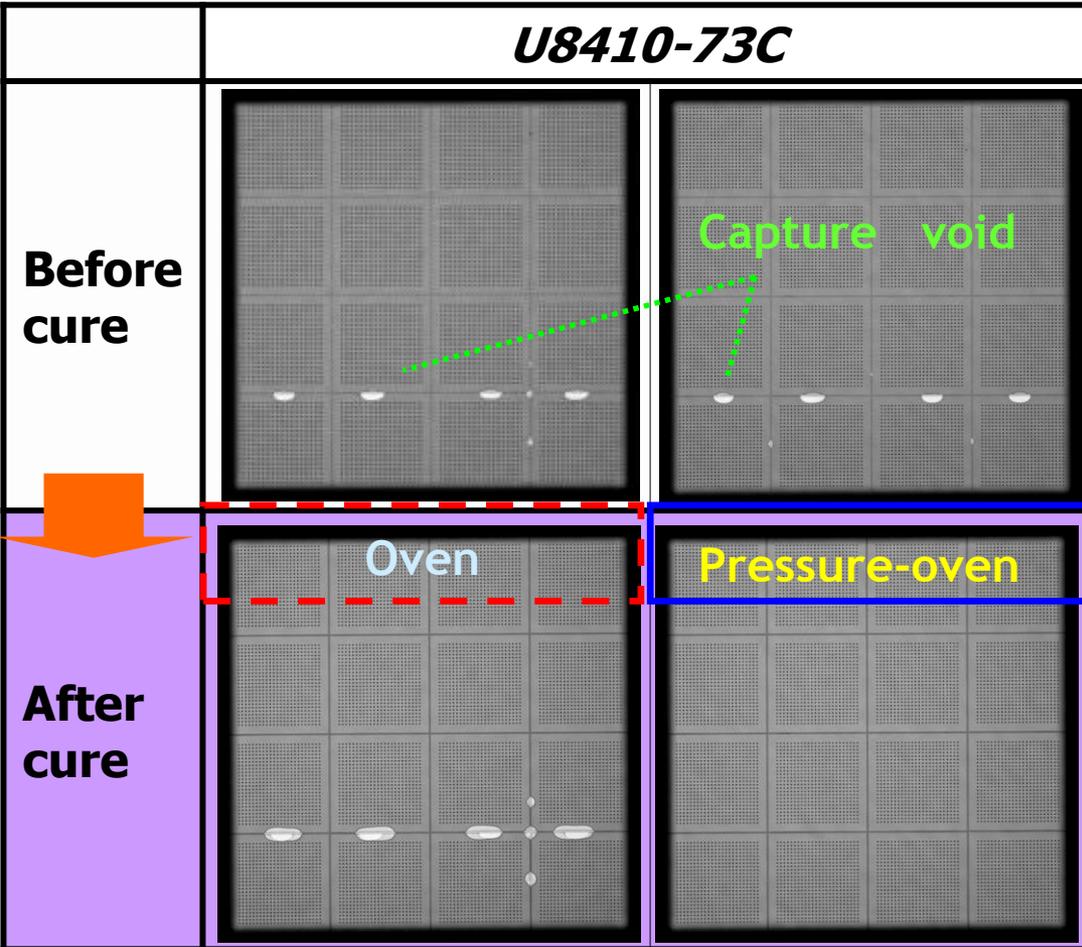


# Void Investigation w/Pressure-oven

## Capture void investigation

### TEST vehicle

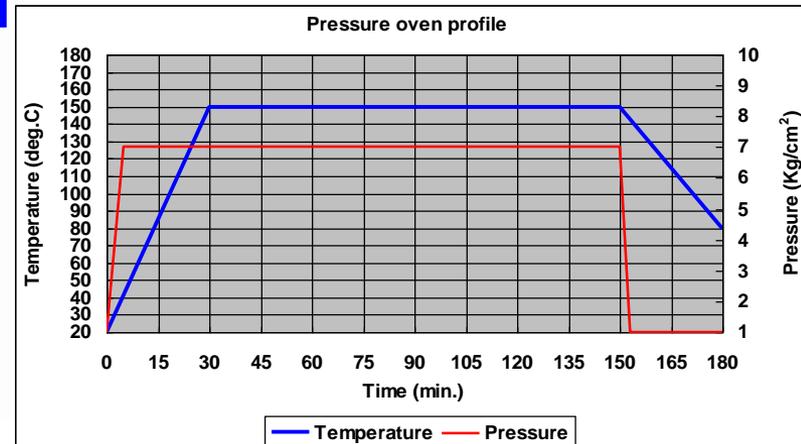
Die size : 20mmx20mmx0.73mm(t)  
Passivation : PI  
Bump material : Sn/3Ag/0.5Cu  
Bump pitch : 175um  
Flux : non-clean  
Substrate size :  
52.5mmx30.0mmx0.73mm(t)



Voids remain.

Voids disappear.

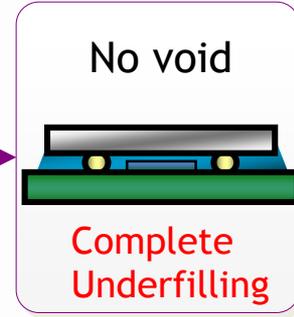
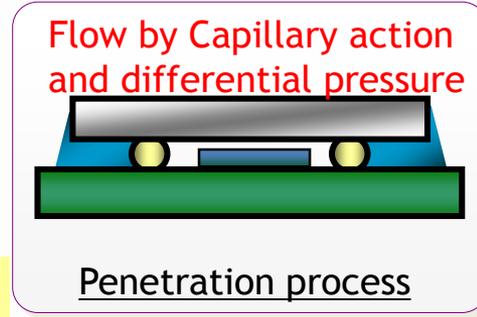
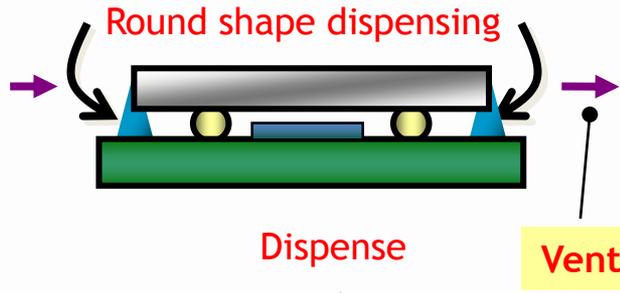
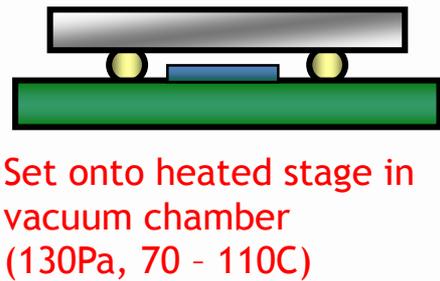
UF	Pressure-oven Type	Pressure value
NAMICS	VFS-60A-JP	7 kg/cm <sup>2</sup>



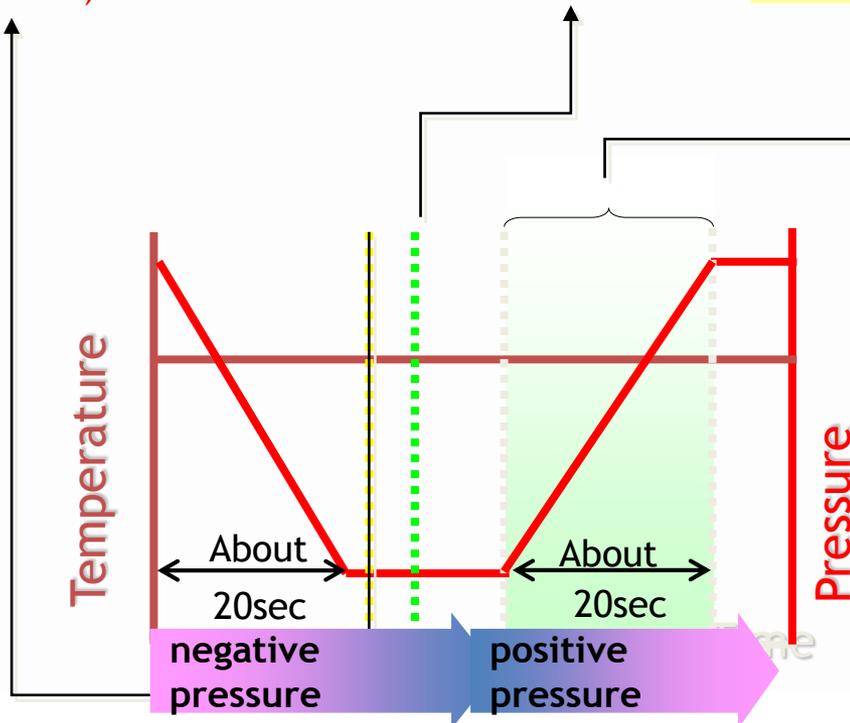
# Void Elimination Process / Vacuum Assisted

Under a vacuum condition

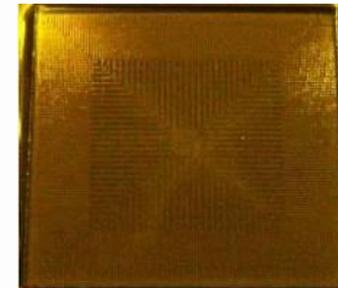
Under atmospheric pressure



Accelerates with positive pressure

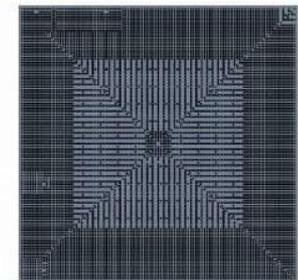


Microscope View



IBM test vehicle  
19 x 19mm die  
150um pitch, Pb free C4

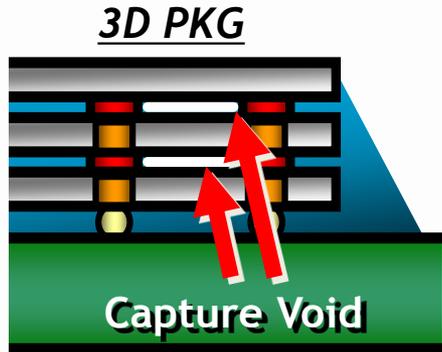
C-SAM View



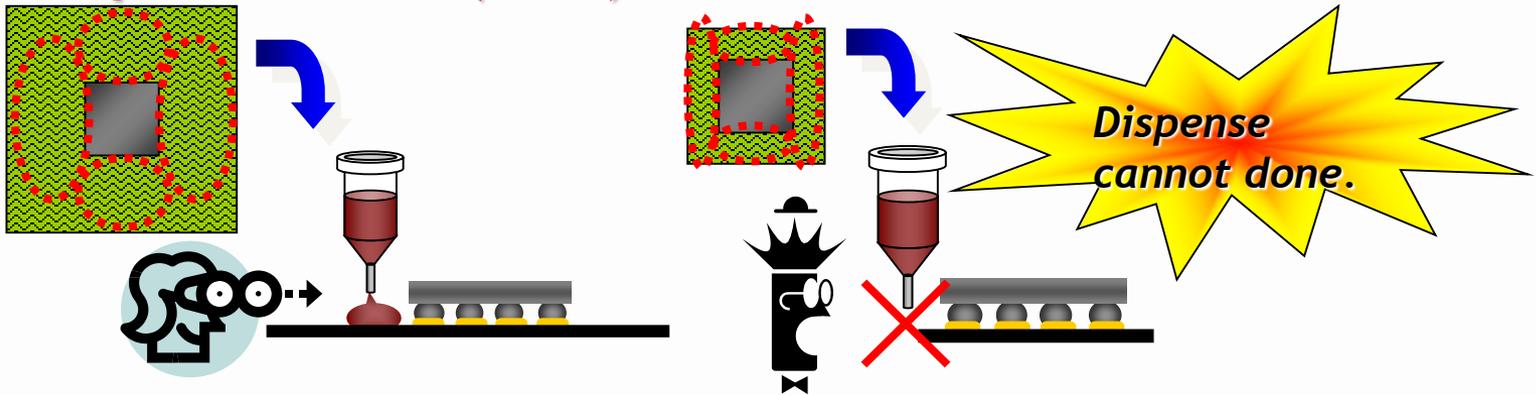
Ref. Horibe, ECTC2011

# Problems for CUF & Advanced PKG

## \*Void Issues

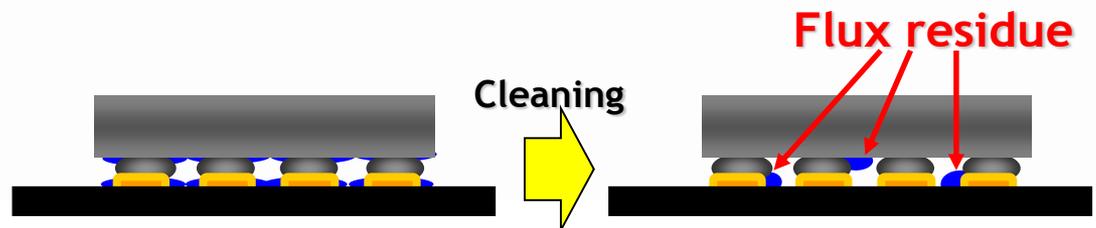


## \*Decrease Dispense Area (KOZ)



## \*Flux Residue Issue

Difficult for the package of a narrow gap to Clean flux.

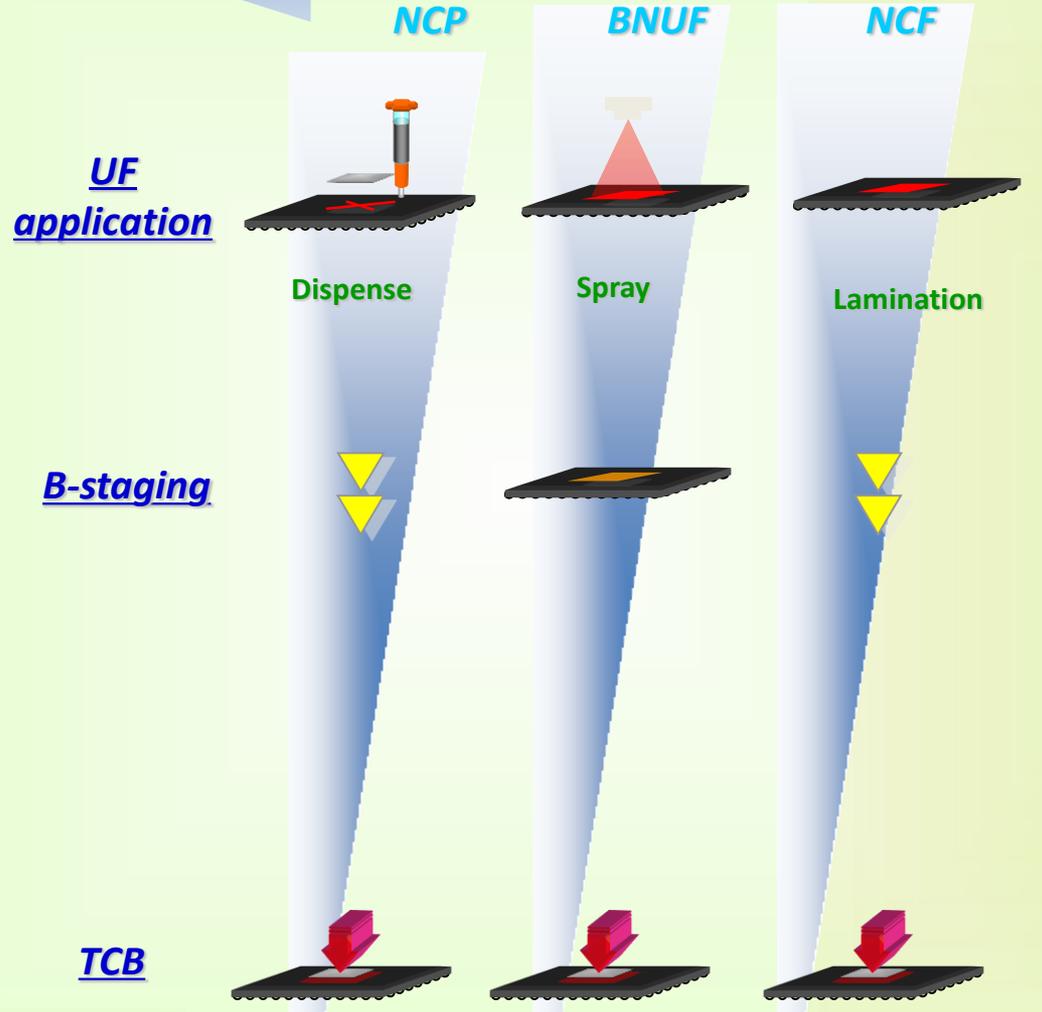


## New Materials For 3D Packaging

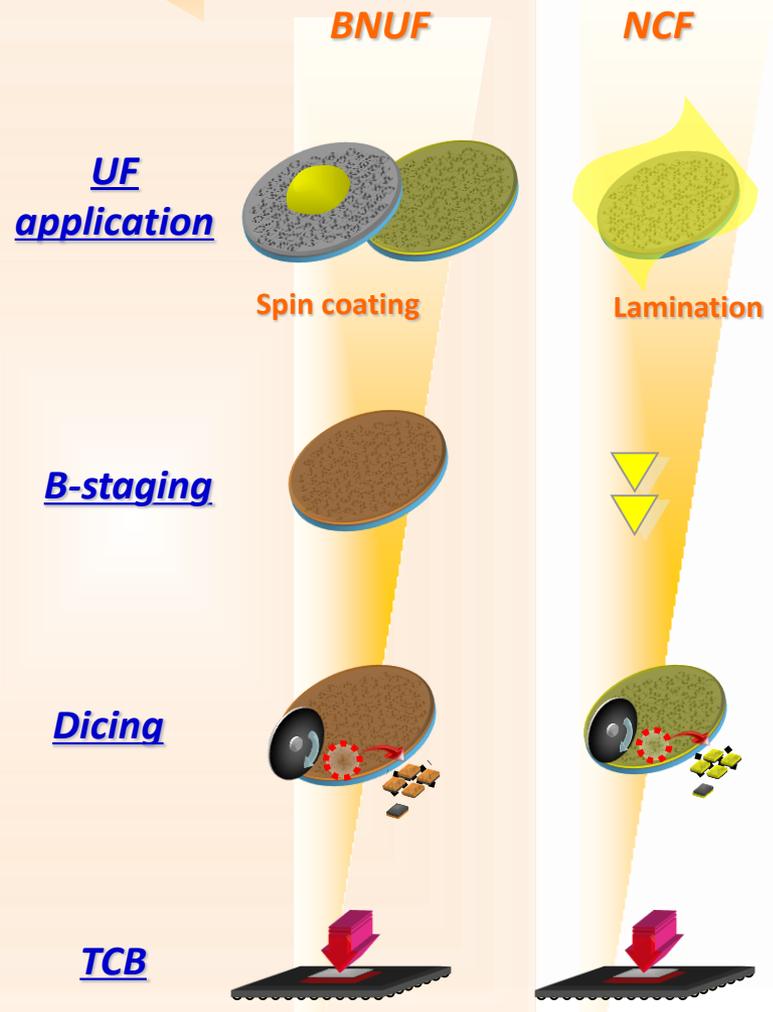
Material	Non Conductive Paste (NCP)	Non Conductive Film (NCF)	B-Stage No Flow UF (BNUF)
Features	<i>Flux free process -&gt; No flux cleaning required</i>		
	<i>No mass reflow process -&gt; Low stress to Low-K layer</i>		
	<i>Excellent penetration for narrow gap &amp; pitch</i>		
	<i>Minimum fillet forming for small KOZ (limited spaces around die)</i>		
	<i>Low strain at bumps can be achieved since bumping connection and NCP curing are done simultaneously while NCP is reinforcing bumps.</i>		
	<ul style="list-style-type: none"> <li><i>•Can be used current dispensing machines</i></li> <li><i>•Will fit package shape</i></li> </ul>	<i>Fine thickness control while providing minimum fillet width; in addition, no risk of underfill creeping on to die.</i>	<i>Excellent penetration into uneven surface and very fine areas because of its low viscosity</i>

# Introduction of Pre-Applied Materials

To  
Substrate

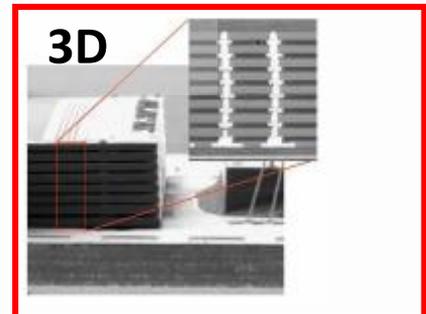
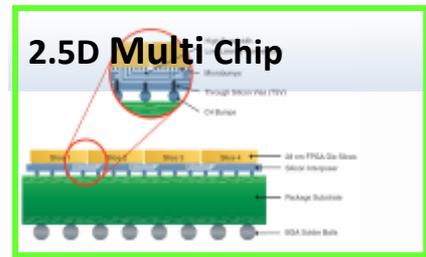
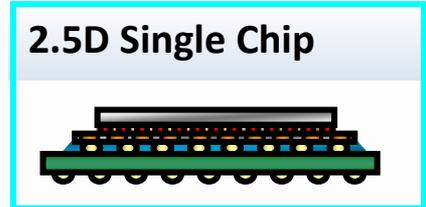
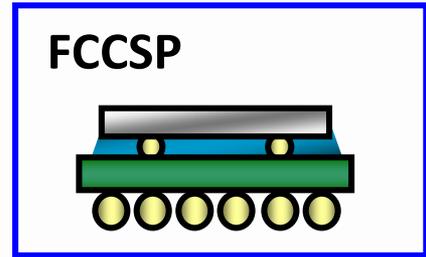


To  
Wafer

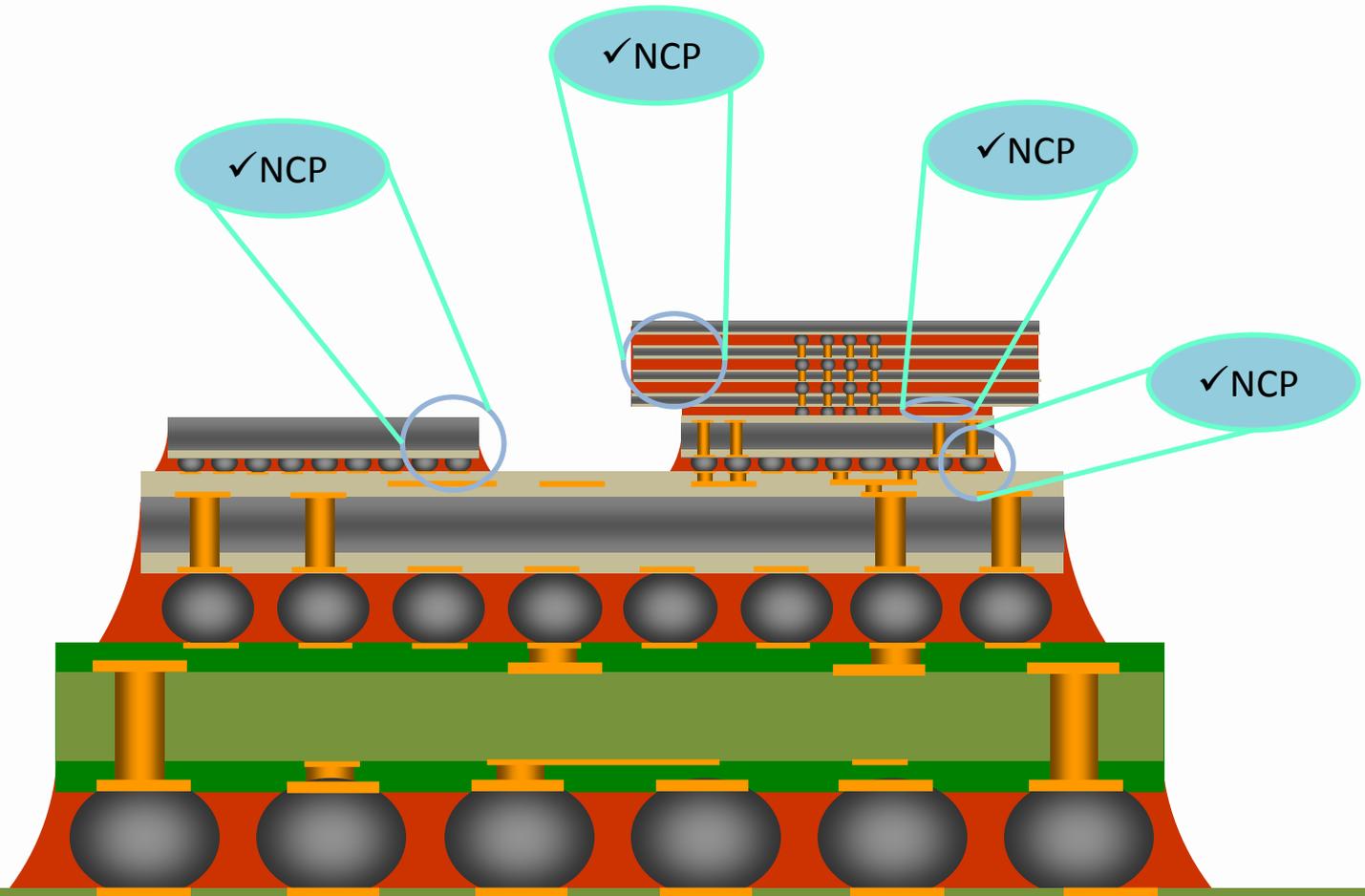


# Target Package for Each PAM

Apply to	Apply to	Application pattern	TCB pattern	Package	Application method	Encapsulation method		
						NCP	NCF	BNUF
Bottom die	Substrate or Interposer		<b>D2S</b>  Die Substrate	FCBGA FCCSP	Dispensing	✓		
					lamination		✓	
					Spray			✓
	Interposer	or  or 	<b>D2D</b>  Die Interposer or Die	2.5D 3D	Dispensing	✓		
					Lamination		✓	
					Spray			✓
Upper die	Wefer		<b>D2W</b>  Die Interposer or Die  <b>D2W</b>  Die Interposer or Die	FCBGA FCCSP 2.5D 3D	Photoliso		✓	
					Spray			✓
					Printing			✓

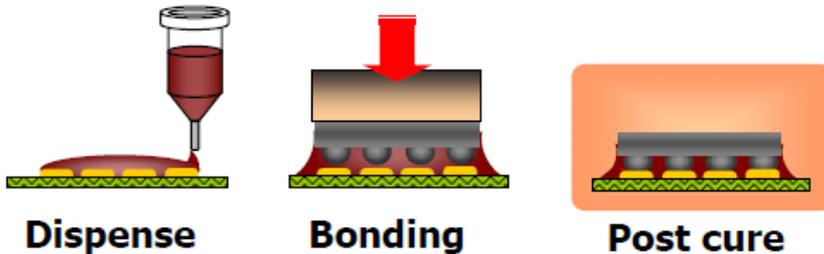


# Non-Conductive Paste (NCP) for 3D Packaging

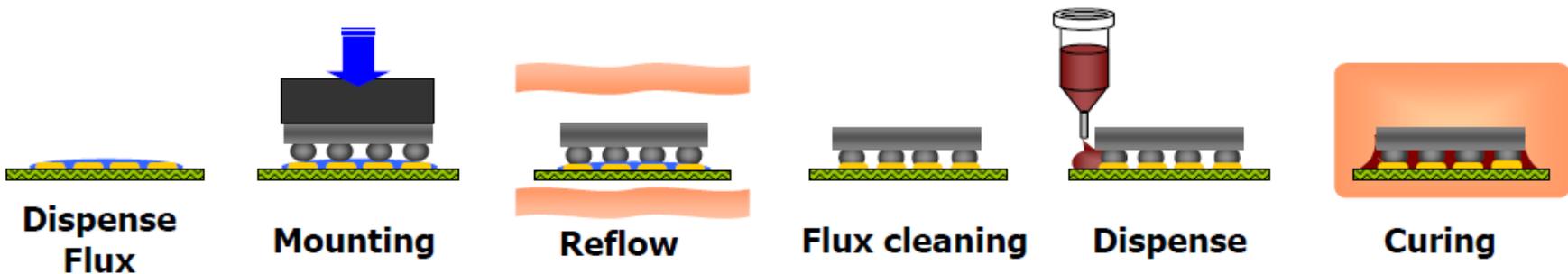


# Non-Conductive Paste (NCP) Process Flow

## NCP



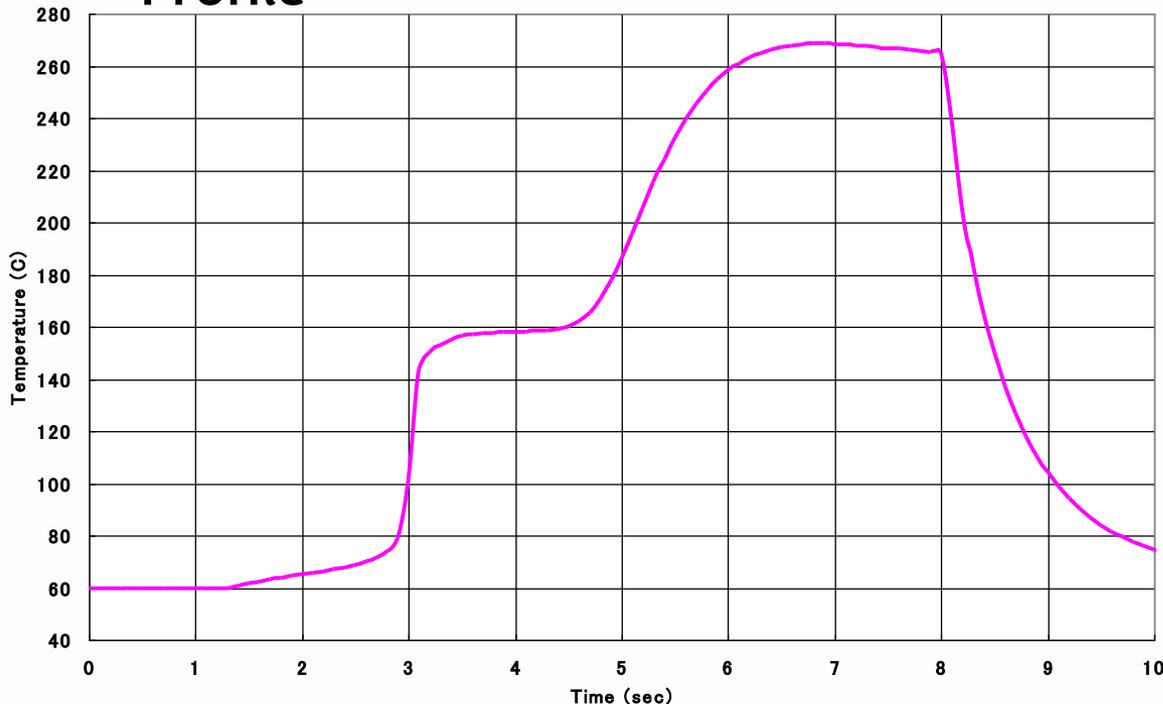
## capillary flow type ( CUF )



What is NCP? A self fluxing low cte material that can be used in conjunction with thermal compression bonding.

## TCB & Test Vehicle Condition

### \*Profile



Equipment : FC3000S ( Toray Engineering)

Stage temp. : 60C

Search height : 0.6 mm

Search speed : 0.6mm/sec

Contact force : 5.4N

Max. force : 24.5N

### \*Test vehicle

•Die size : 7.3mmX7.3mmX0.125mm(t)

•Bump structure : Cu pillar with solder (solder / Pillar = 15um/30um)

•Number of bump : 544

•Bump layout : Peripheral

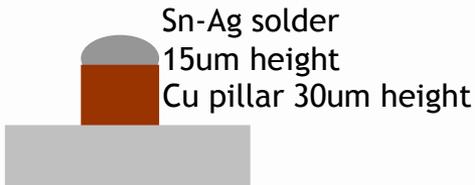
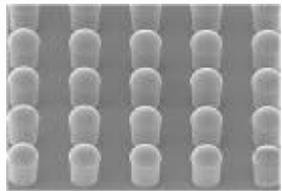
•Sub. thickness : 0.36mm

• Lead finish : Cu/OSP (ENTEK)

# Reliability Test Results

## Chip

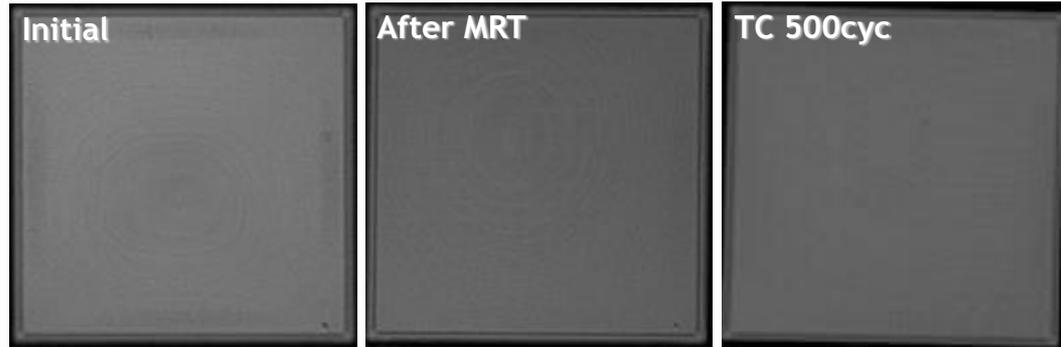
Material		Si
Size		7.3mm x 7.3mm
Thickness		125 um
Bump	Substance	Cu pillar w/ Sn-Ag solder
	Alignment	Peripheral
	Pitch	50 um
	# of bumps	544



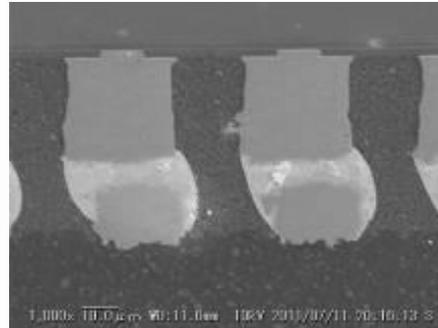
## Substrate

Substrate material	Organic substrate
Thickness	360 um
Electrode material	Cu/Sn-Ag solder

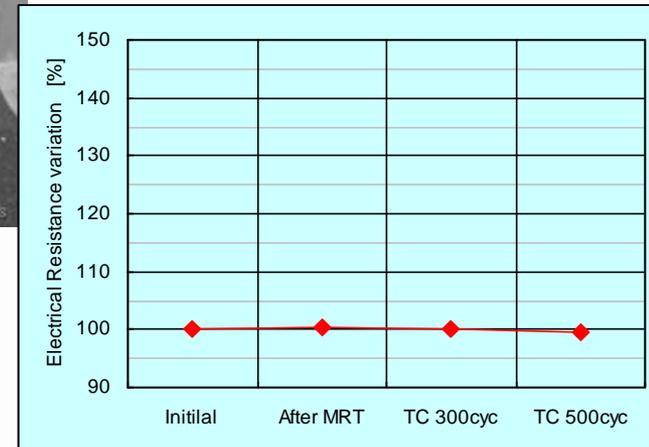
## C-SAM



## Cross-section at Initial

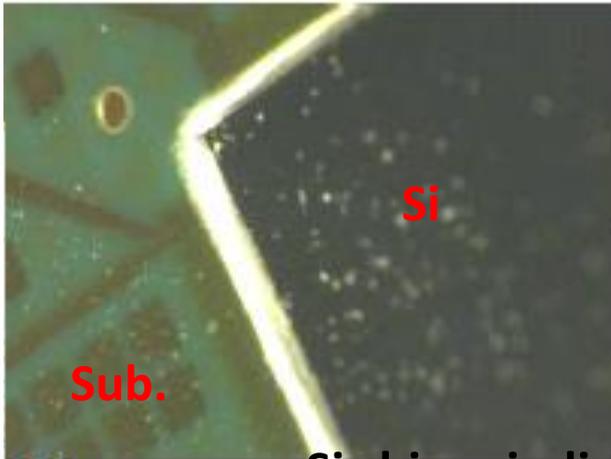


## Electrical Resistance

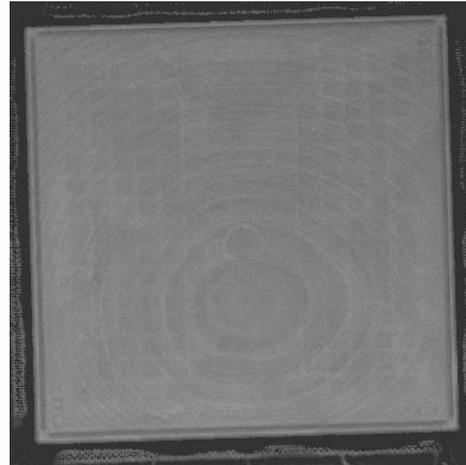


## Result

Fillet view

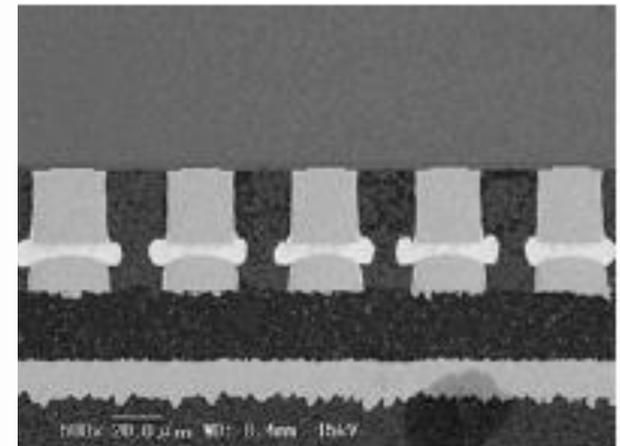
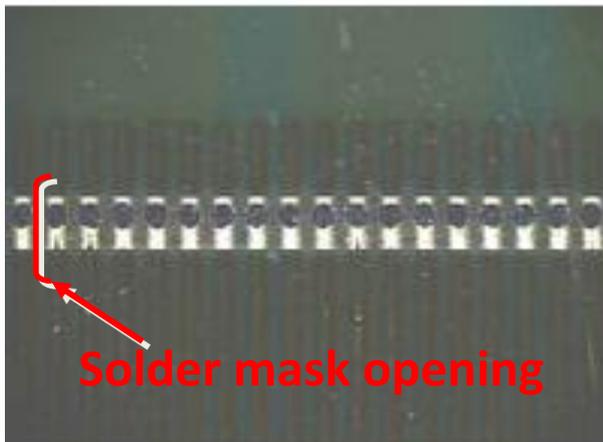


SAT view

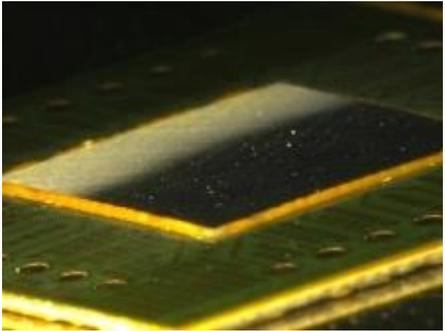
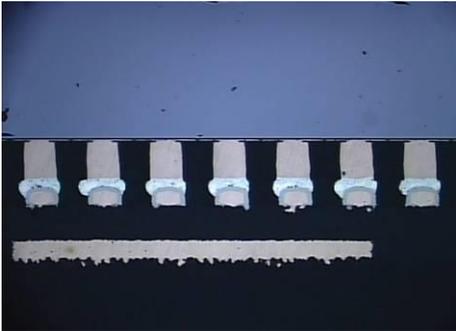
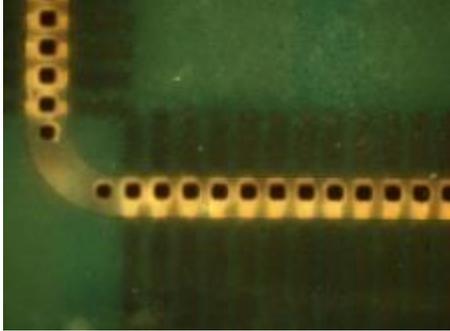


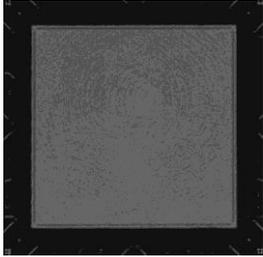
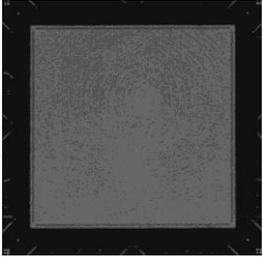
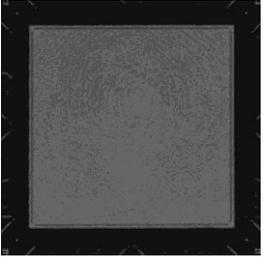
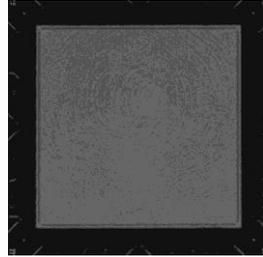
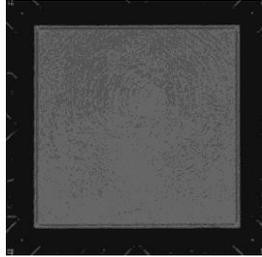
Si chip grinding view

X-section view



# Reliability Evaluation Results

Appearance	Connection	Void (solder resist opening)
		

	After cure	Reflow	Thermal cycle		
			100 cyc	500 cyc	1000 cyc
C-SAM					

Reflow test = 30deg.C/60%RH 192hrs , 260deg.C reflow X 3times

Thermal cycle test = -55deg.C/125deg.C

## ***Thermal Compression w/ Capillary Flow (TCCUF)***

TCCUF, which is abbreviation for “*Thermal Compression with Capillary*

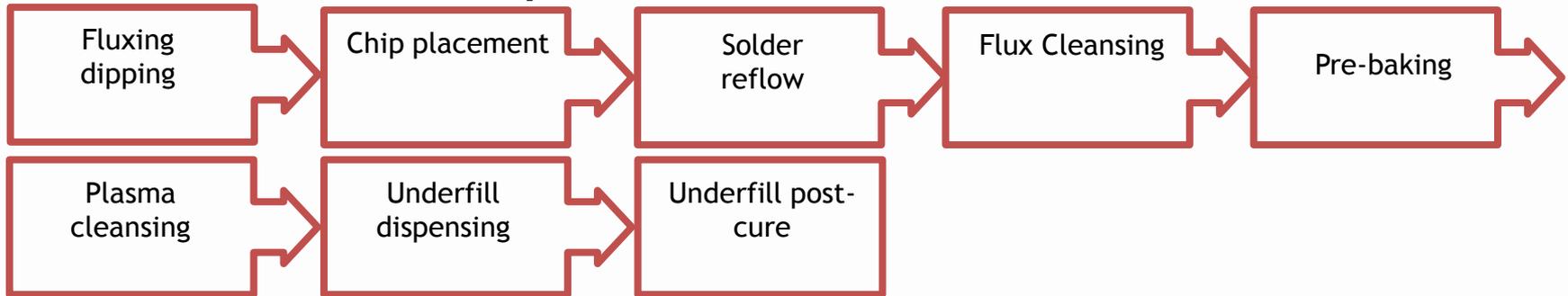
*Underfill*” is a hybrid fabrication method that used Capillary Underfill and a

Thermal Compression Bonding process. The aim of this process is not only to keep assembly cost down, but also to ensure yield is not affected by filler entrapment or voiding with Non-Conductive Paste.

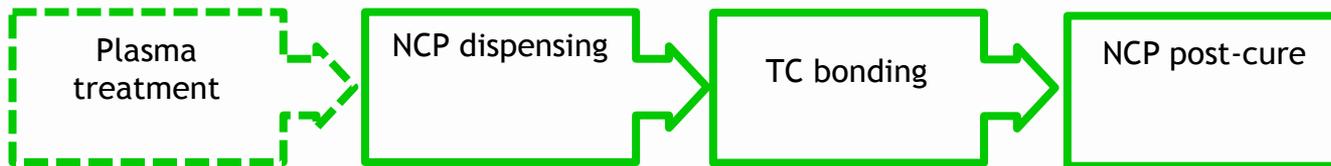
TCCUF, compared with other processes, is very simple. The hybrid process did not require a pre-baked, water clean, and flux residue removal. Thus, not only it is simplified process, but also saved total process time & increased yield.

## Process Comparison

### ➤ Conventional underfill process



### ➤ Thermal compression with nonconductive paste (TCNCP)

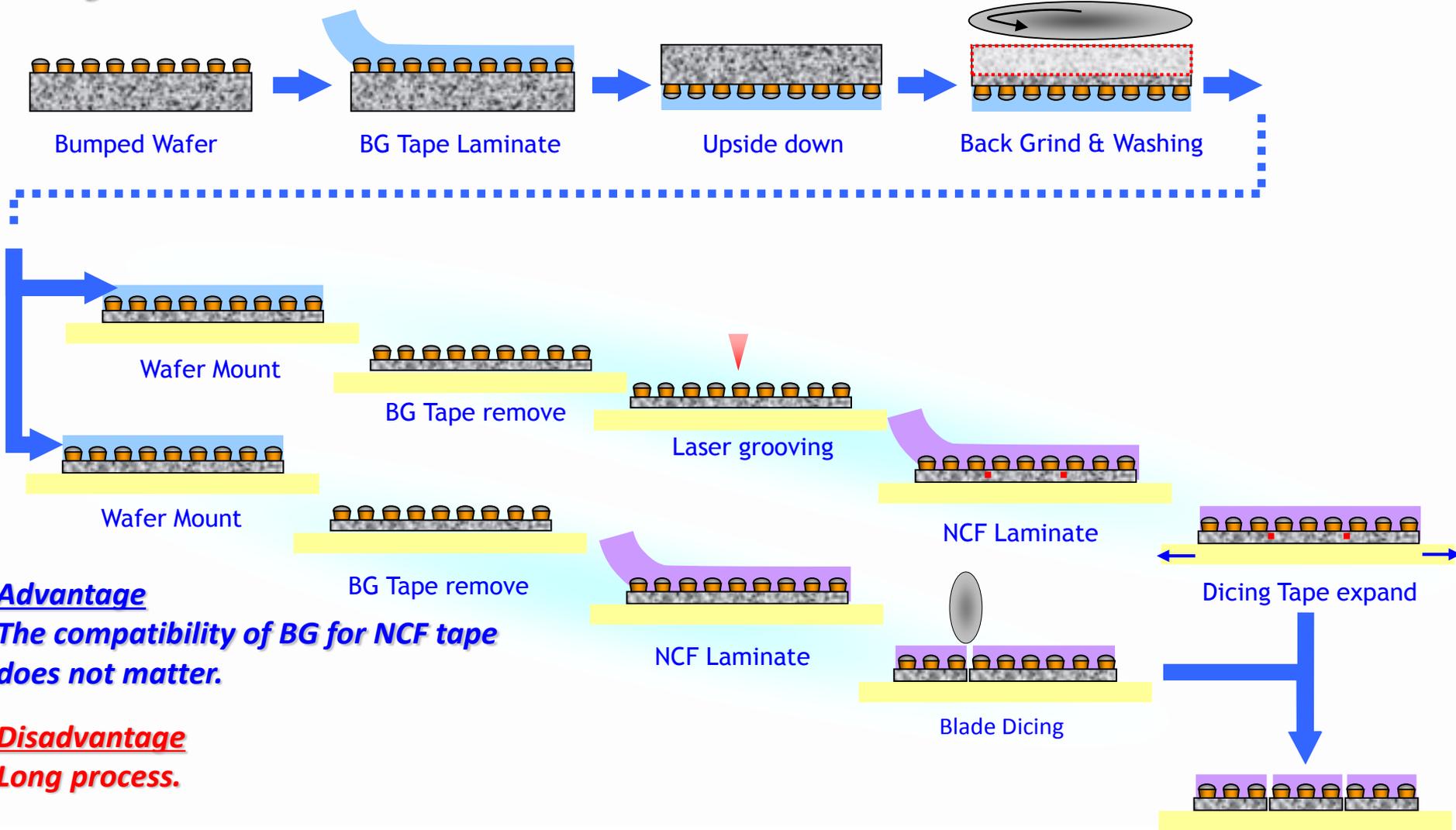


### ➤ Thermal compression reflow with capillary underfill process (TCCUF)





# Wafer Level Process



**Advantage**

*The compatibility of BG for NCF tape does not matter.*

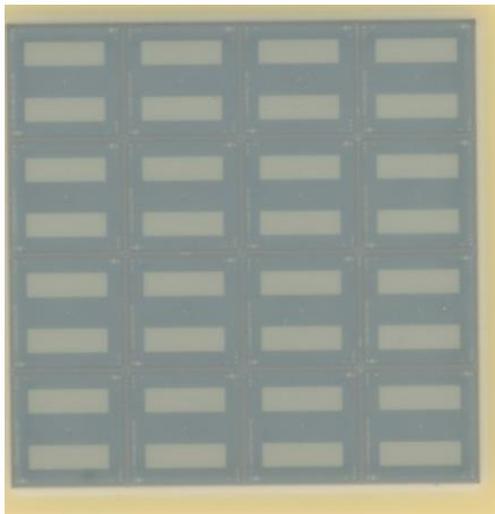
**Disadvantage**

*Long process.*

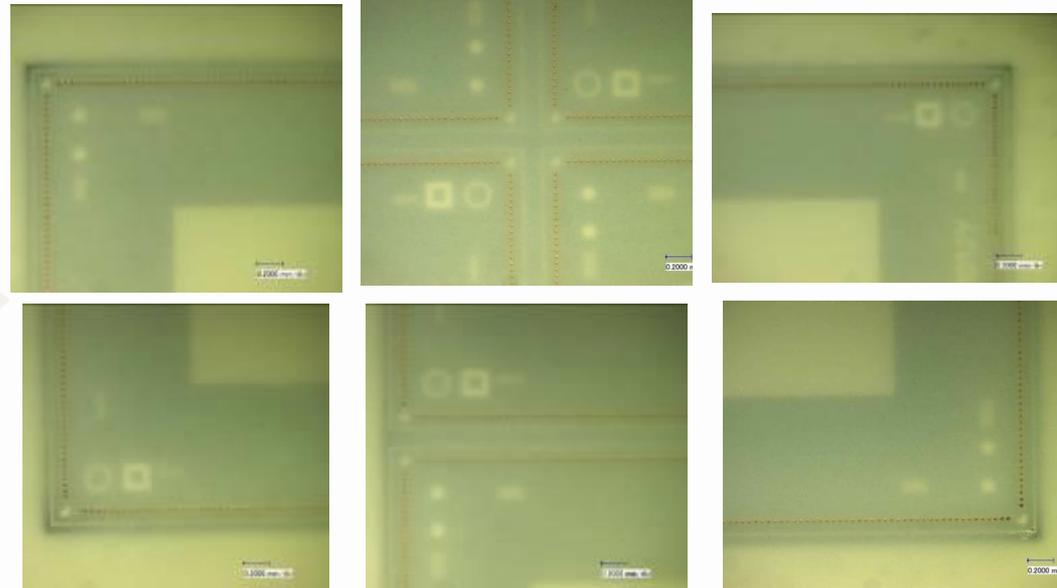
# *Lamination Capability*



Test piece after lamination



Observation



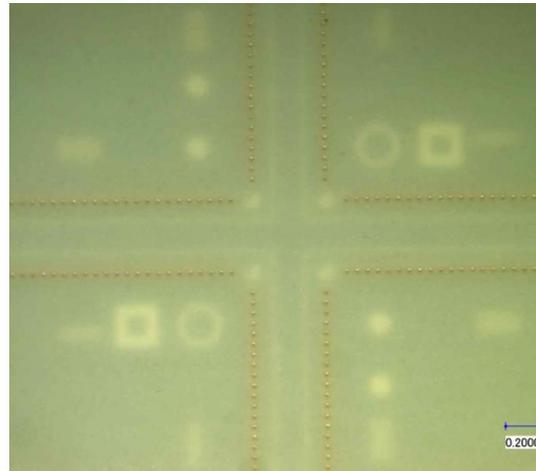
**Good lamination capability has been confirmed.**

# Dicing Capability

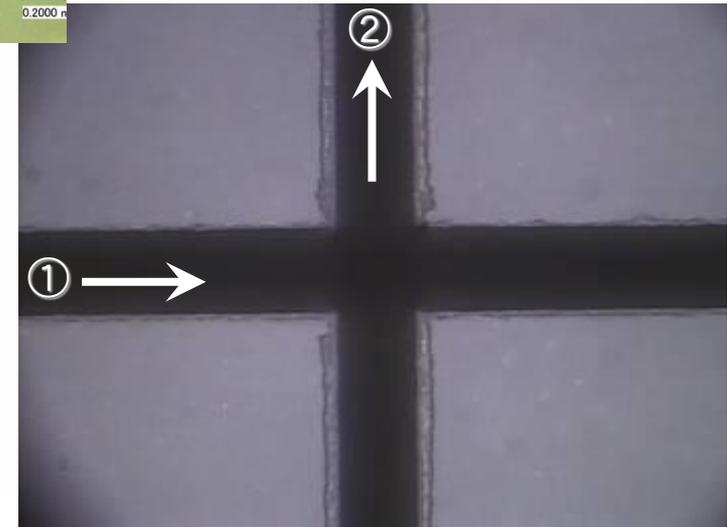
After Lamination



**Blade Dicer**



After Dicing



# Process Condition for NCF

Baking Substrate	Temperature	125 deg.C
	Time	3 hours
	Equipment	Hot Air Oven



Film Laminate	Temperature	70 - 80 deg.C
	Press Force	0.3 - 0.7MPa
	Press Time	0.5 - 3 min.
	Vacuum Level	< 10kPa

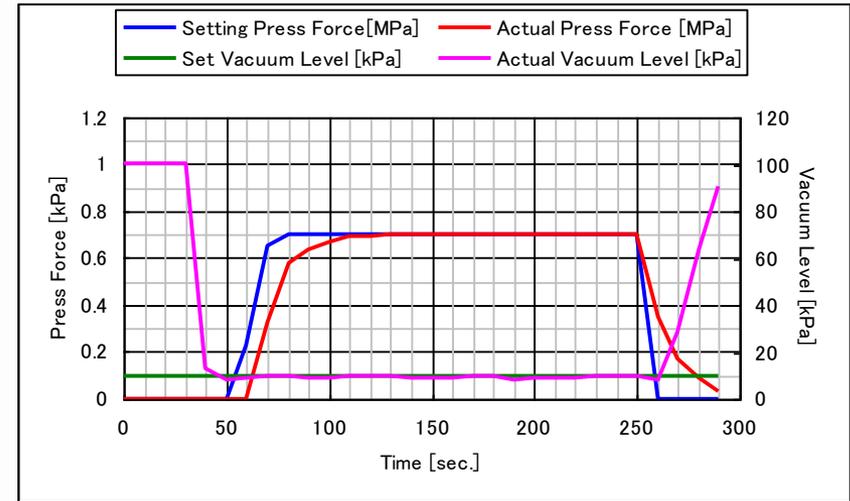


Bonding	Stage Temperature	80 deg.C
	Bonding Force	0.3 - 0.6 N/mm <sup>2</sup> (3 - 6 g/bump)
	Profile	Refer to the chart

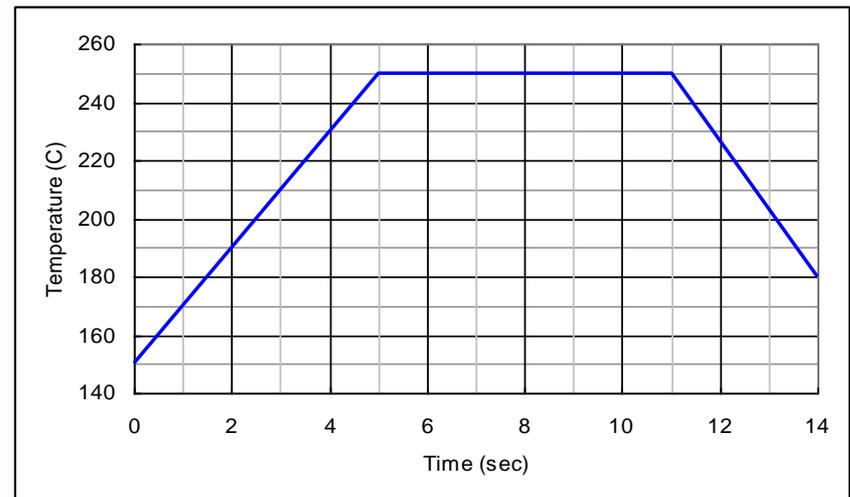


Curing	Temperature	165 deg.C
	Time	1 hours
	Equipment	Hot Air Oven

## Laminate Condition Example



## Bonding Profile

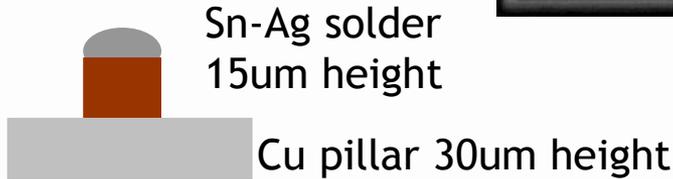
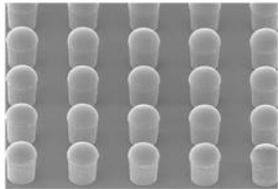


Profile is actual temperature between Chip and Substrate.

## ***NCF Reliability Evaluation Results***

### ***Chip***

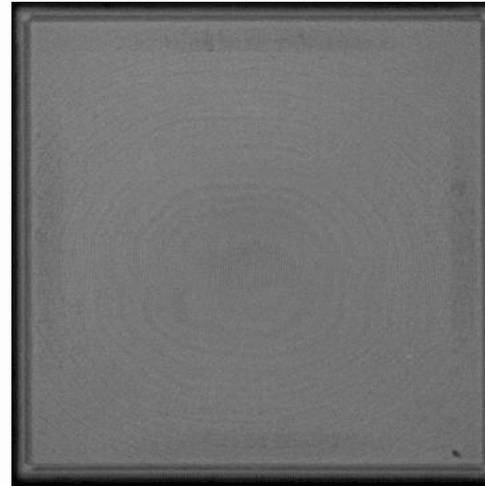
<b>Material</b>		<b>Si</b>
<b>Size</b>		<b>7.3mm x 7.3mm</b>
<b>Thickness</b>		<b>125 um</b>
<b>Bump</b>	<b>Substance</b>	<b>Cu pillar w/ Sn-Ag solder</b>
	<b>Alignment</b>	<b>Peripheral</b>
	<b>Pitch</b>	<b>50 um</b>
	<b># of bumps</b>	<b>544</b>



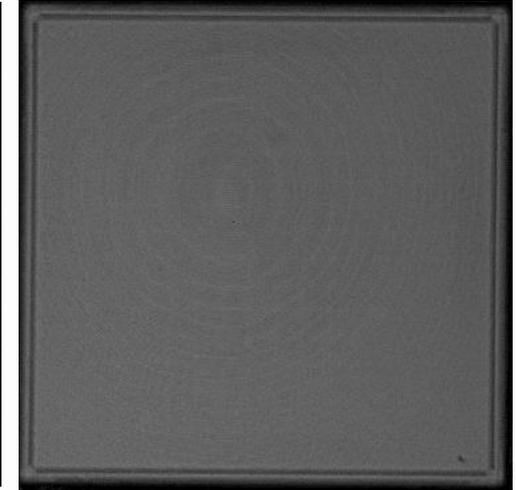
### ***Substrate***

<b>Substrate material</b>	<b>Organic substrate</b>
<b>Thickness</b>	<b>360 um</b>
<b>Electrode material</b>	<b>Cu/Sn-Ag solder</b>

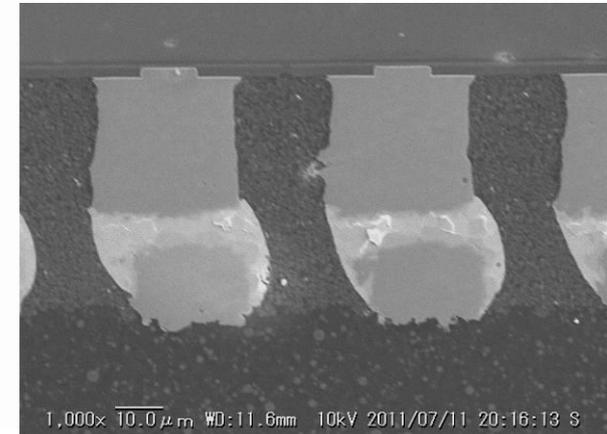
### ***Initial***



### ***After MRT***



### ***Cross-section***



Reliability Test Condition  
MSL Lv3. (60 deg.C/60%RH/ 40hr.) □260 dig.C Reflow/ 3times

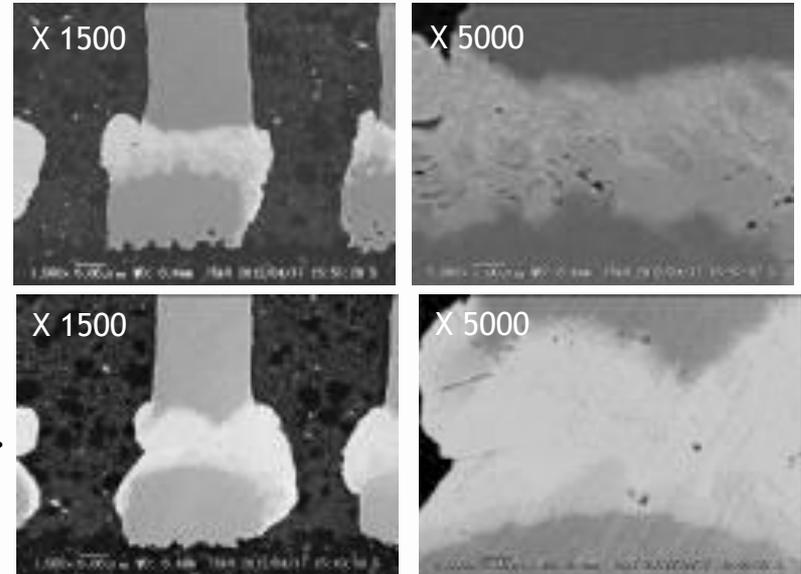
# TCB

## Substrate

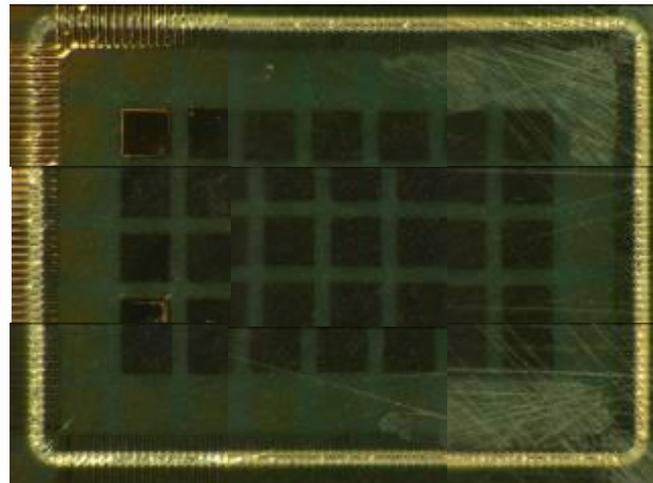
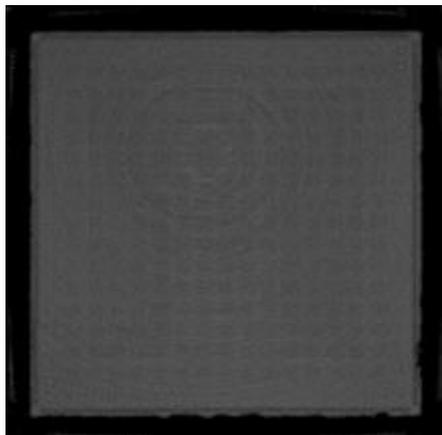
Substrate material	Organic substrate
Thickness	360 um
Electrode material	Cu/Sn-Ag solder

Good connection by solder melting was formed.  
It was conformed no void.

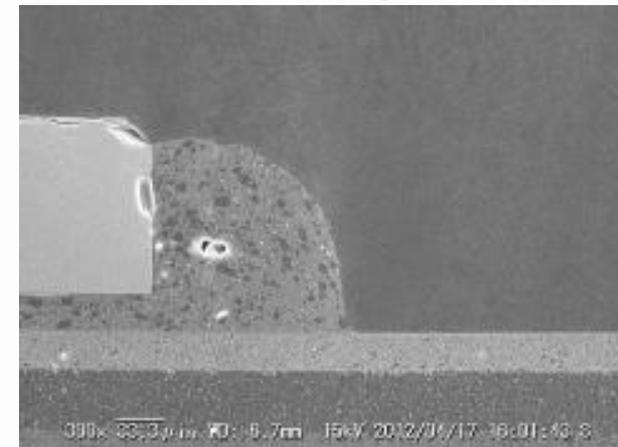
## Interconnection



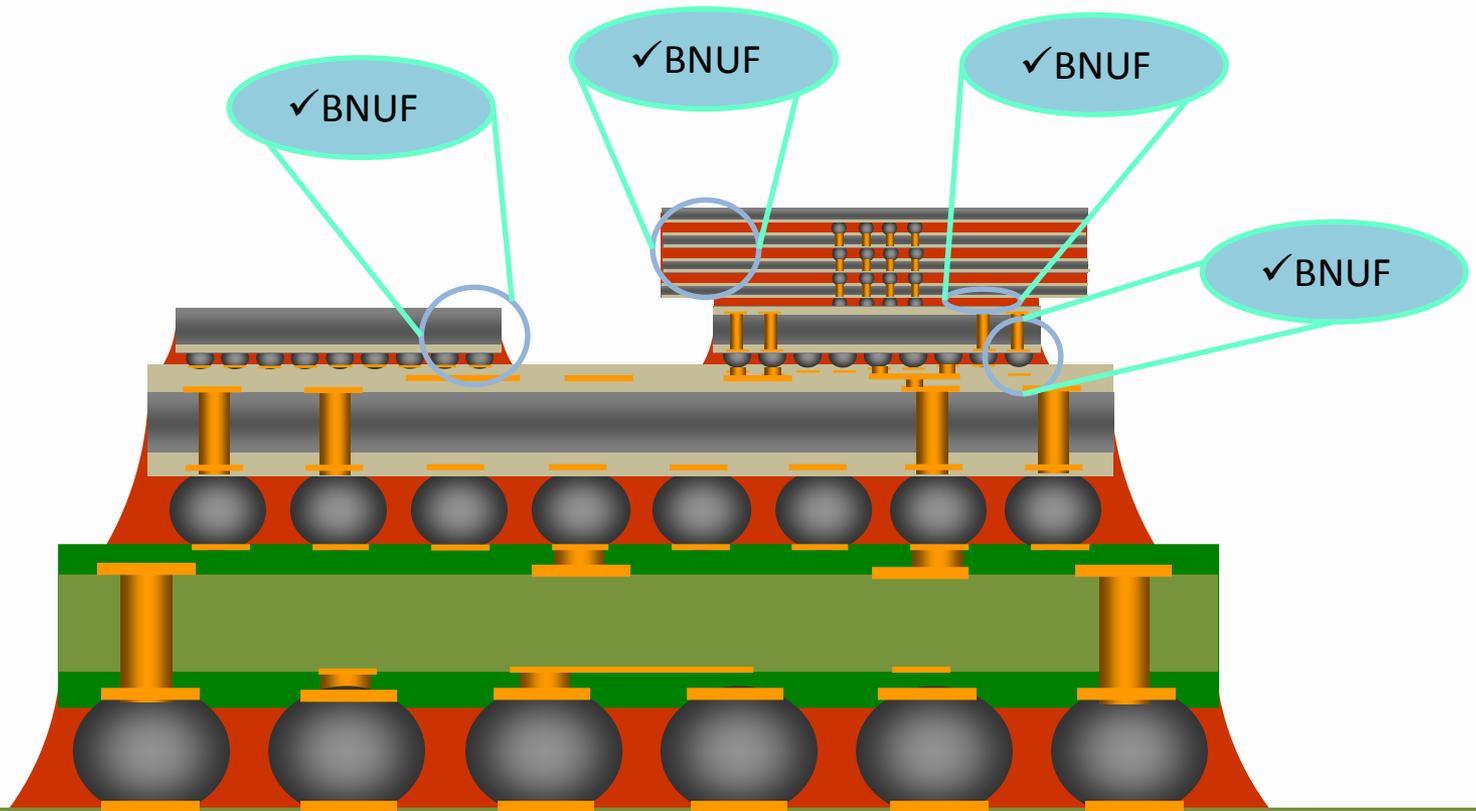
## Void observation by C-SAM and Surface Grinding



## Fillet shape



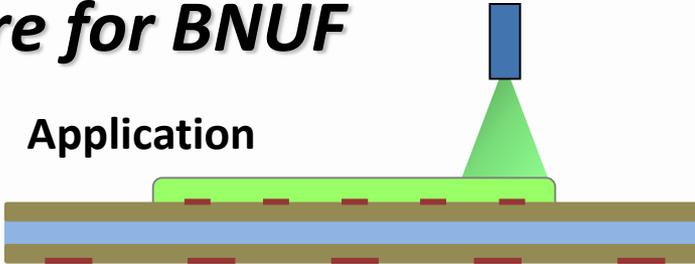
## ***B-Stage No Flow Underfill (BNUF) for 3D Packaging***



## Key Feature for BNUF

Liquid

Application

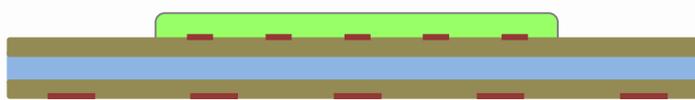


Planer application [Dispensing, Spray, etc.]

Excellent surface coverage

B-stage

B-staging

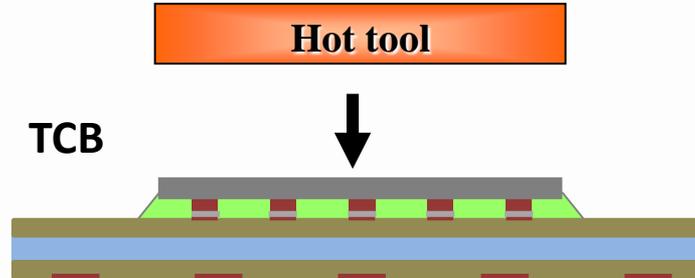


Tack free @ RT

Partial cure

Hot tool

TCB



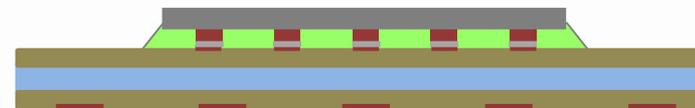
Tacky @ TCB process

Self fluxing

High throughput for TCB process

Full cure

Post cure

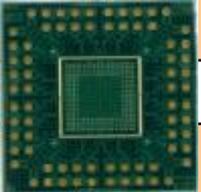


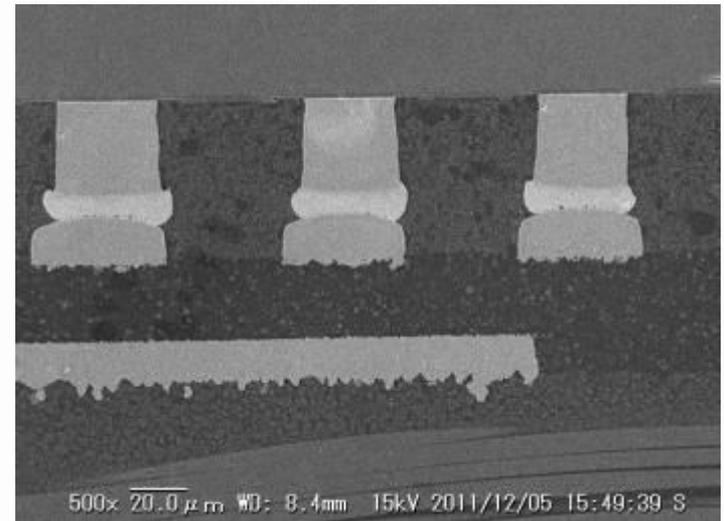
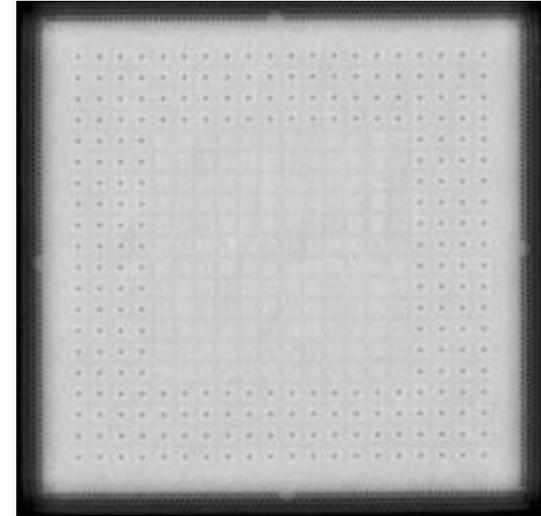
## Cross Section of BNUF after TCB

### 【Die】

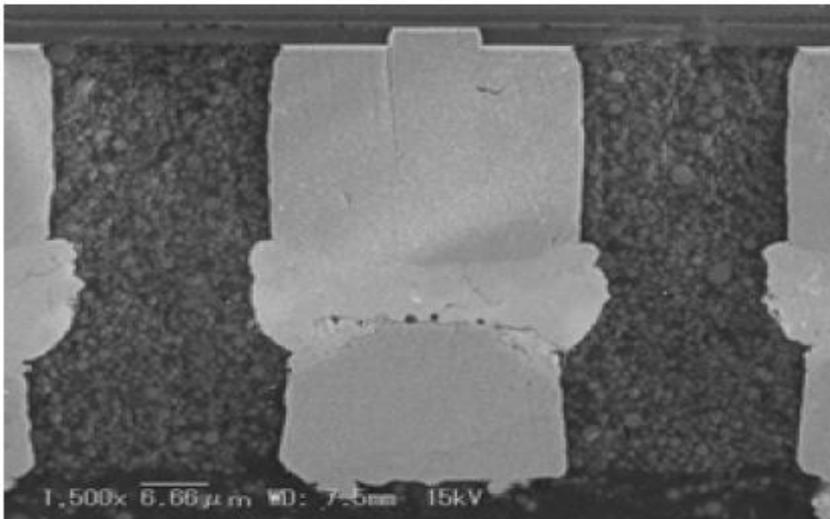
Size (mm)	7.3 × 7.3	
Bump	Cu-pillar[t=30um] w/ Sn-Ag cap[t=15um]	
Bump pitch (um)	peripheral 80	
	core bump 300	
Passivation	PI(PI2727)	
Number of bump	peripheral 648	
	core bump 400	

### 【substrate】

Surface treatment	Ni/Au	
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## Filler Entrapment?



**1000hrs**

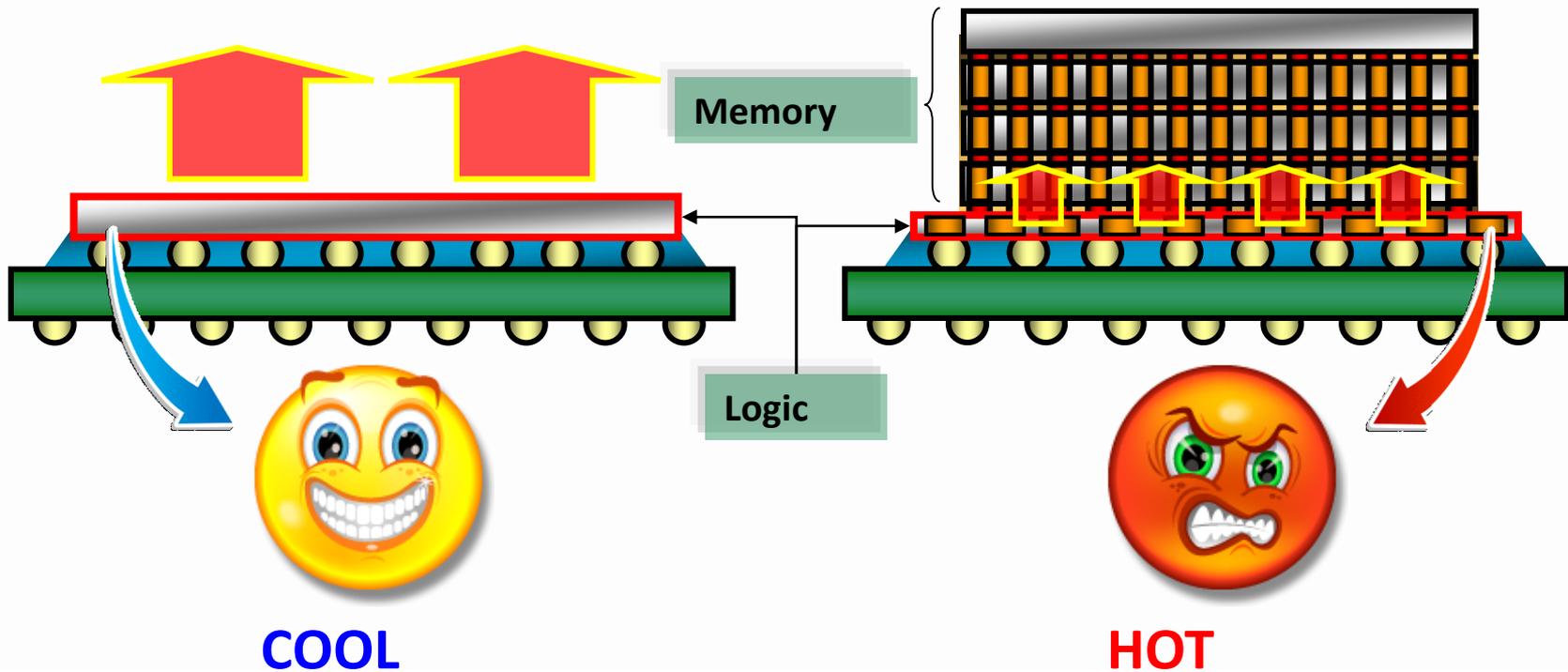
Thus far filler entrapment during the thermal compression bonding process has been seen, however there has been no ill effects to mechanical or electrical reliability.

Further finite element analysis might be necessary to determine when mechanical and electrical failures begin to occur.

## ***Thermal Conductivity is Important for 3D packages***

Flip chip PKG

3D PKG



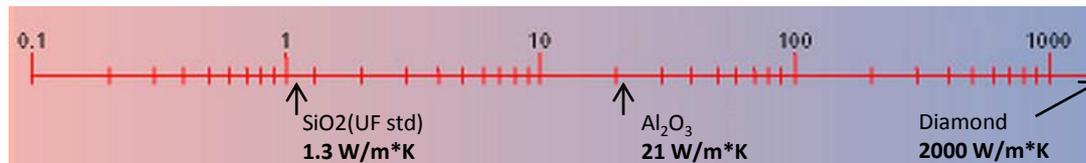
Since 3D package represents multiple heat sources that results in high thermal dispersion, underfill must improve thermal performance in order to collaborate with complex package design.

## How Underfill Thermal Conductivity Can Be Improved



While replacing silica fillers with high thermal conductive fillers, underfill thermal conductivity improves.

Thermal conductivity (W/m\*K)



Thermal conductivity of Al<sub>2</sub>O<sub>3</sub> is 16 times higher than SiO<sub>2</sub>.

### Theory of Bruggeman

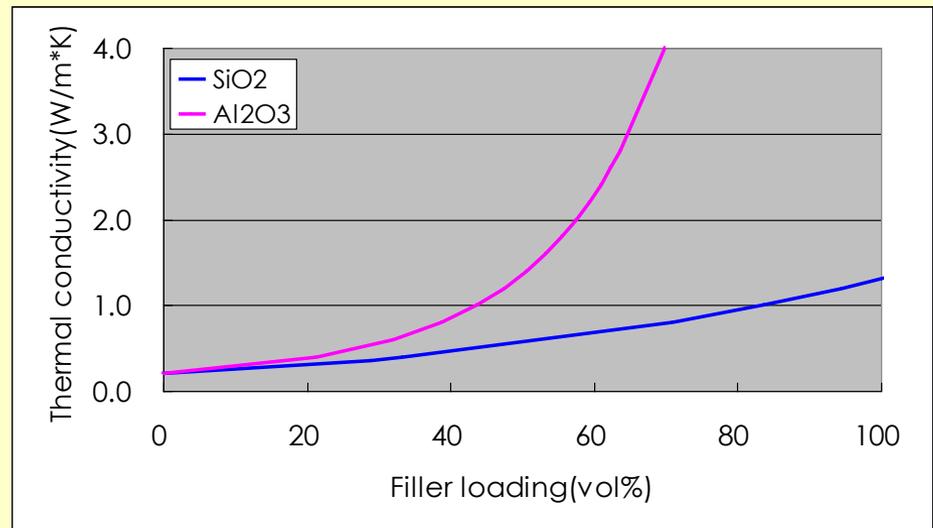
$$1 - \phi = \frac{\lambda_e - \lambda_d}{\lambda_c - \lambda_d} \left[ \frac{\lambda_c}{\lambda_e} \right]^{1/3}$$

$\phi$  : Volume fraction of filler

$\lambda_e$  : Thermal conductivity of composite

$\lambda_c$  : Thermal conductivity of matrix

$\lambda_d$  : Thermal conductivity of filler



## Summary

- ✓ *CUF continues to evolve providing narrow gap penetration and jet dispense stability for high volume manufacturing.*
- ✓ *Voiding risk has been minimized due to pressure cure ovens and vacuum assist processing.*
- ✓ *Pre-applied materials, NCP, NCF, BNUF have different characteristics from CUF and can provide new assembly opportunities moving forward compared to std reflow technology. Hybrid manufacturing processes such as TCCUF also offer distinct advantages.*
- ✓ *Filler Entrapment during TCB thus far is not a major concern but warrants further study.*
- ✓ *High thermal conductivity materials are being examined once again in detail for 2.5/3D applications.*

